

Miscellaneous Integrated Circuits

ECG2046 8-Pin DIP See Fig. L97
60 Hz Time Base Generator,
Input Freq=3.58 MHz (CMOS),
VDD=15 V Max

DIVIDER OUTPUT 1 8 VDD
VSS 2 7 TUNER OUTPUT
NC 3 6 OSC OUTPUT
NC 4 5 OSC INPUT

ECG2047 16-Pin DIP See Fig. L112
FSK Modem, 0-600 Baud, (CMOS),
VDD=15 V Max

RX CAR 1 16 VDD
ST 2 15 TTLD
OSCOUT 3 14 TYPE
OSCIN 4 13 ECHO
RESET 5 12 TX ENABLE
RX RATE 6 11 TX DATA
RX DATA 7 10 MODE
VSS 8 9 TX CAR

ECG2049 40-Pin DIP See Fig. L125
LCD 3 1/2 Digit A/D Converter, Low Power
(CMOS), V±=15 V Max
ECG2051
LCD 3 1/2 Digit A/D Converter (CMOS)

V+ 1 40 OSC 1
D1 2 39 OSC 2
C1 3 38 OSC 3
B1 4 37 TEST
A1 5 36 REF HI
F1 6 35 REF LO
G1 7 34 C+ REF
E1 8 33 C- REF
D2 9 32 COMMON
C2 10 31 IN HI
B2 11 30 IN LO
A2 12 29 A/Z
F2 13 28 BUFF
E2 14 27 INT
D3 15 26 V-
B3 16 25 G2 (TENS)
F3 17 24 C3
E3 18 23 A3 (100'S)
(1000) AB4 19 22 G3
- POL 20 21 BP

ECG2050 40-Pin DIP See Fig. L125
LED 3 1/2 Digit A/D Converter (CMOS),
V=±5 V Typ

V+ 1 40 OSC 1
D1 2 39 OSC 2
C1 3 38 OSC 3
B1 4 37 TEST
(UNITS) A1 5 36 REF HI
F1 6 35 REF LO
G1 7 34 C+ REF
E1 8 33 C- REF
D2 9 32 COMMON
C2 10 31 IN HI
(TENS) B2 11 30 IN LO
A2 12 29 A/Z
F2 13 28 BUFF
E2 14 27 INT
D3 15 26 V-
B3 16 25 G2 (TENS)
(100'S) F3 17 24 C3
E3 18 23 A3 (100'S)
(1000) AB4 19 22 G3
- POL 20 21 DIGITAL GND

ECG2052 28-Pin DIP See Fig. L124A
3 1/2 Digit A/D Converter with Multiplexed
7-Segment Output (CMOS), Vcc=5 V Typ

VCC 1 28 Sg
ANALOG VCC 2 27 Sf
Sd 3 26 Sg
Sc 4 25 GND
Sb 5 24 DIGIT 1 (MSD)
Sa 6 23 DIGIT 2
OFLO 7 22 DIGIT 3
CONVERSION COMPLETE 8 21 DIGIT 4 (LSD)
START CONVERSION 9 20 fOUT
SIGN 10 19 fIN
VFILTER 11 18 VREF
VIN - 12 17 SW1
VIN + 13 16 SW2
VFB 14 15 ANALOG GND

ECG2053 20-Pin DIP See Fig. L118A
8-Bit MPU Compatible A/D Converter
(CMOS), Vcc=5 V Typ

CS 1 20 VCC (OR VREF)
RD 2 19 CLK R
WR 3 18 DB0 (LSB)
CLK IN 4 17 DB1
INTR 5 16 DB2
VIN + 6 15 DB3
VIN - 7 14 DB4
A GND 8 13 DB5
VREF/2 9 12 DB6
D GND 10 11 DB7 (MSB)

ECG2054 16-Pin DIP See Fig. L112
3 Digit A/D Converter, V+ =5 V Typ

BCD OUTPUTS { 21 1 16 23 } BCD OUTPUTS
DIGIT SELECT OUTPUTS { NSD 3 14 V+
MSD 4 13 GAIN ADJ
LSD 5 12 INTEGRATING CAP
HOLD/BYPASS 6 11 HIGH INPUT
GND 7 10 LOW INPUT
ZERO ADJ 8 9 ZERO ADJ

ECG2055 24-Pin DIP See Fig. L122
3 1/2 Digit A/D Converter (CMOS),
VDD, VEE=±5 V Typ, VSS=GND

VAG 1 24 VDD
VREF 2 23 Q3
VX 3 22 Q2
Ri 4 21 Q1
Ri/Ci 5 20 Q0
Ci 6 19 DS1
CO1 7 18 DS2
CO2 8 17 DS3
DU 9 16 DS4
CLK 1 10 15 OR
CLK 0 11 14 EOC
VEE 12 13 VSS

ECG2056 16-Pin DIP See Fig. L112
D/A Converter, Vcc=+5 V Typ,
VEE=-15 V Typ

OUTPUT RANGE CON 1 16 COMPENSATION
GND 2 15 VREF -
VEE 3 14 VREF +
IO 4 13 VCC
MSB A1 5 12 A8 LSB
A2 6 11 A7
A3 7 10 A6
A4 8 9 A5