Darlington Transistors

NPN Silicon

Features

• These are Pb–Free Devices*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	55	Vdc
Collector-Base Voltage	V _{CBO}	80	Vdc
Emitter-Base Voltage	V _{EBO}	12	Vdc
Collector Current – Continuous	Ι _C	1.0	Adc
Total Power Dissipation @ $T_A = 25^{\circ}C$ Derate above $T_A = 25^{\circ}C$	PD	625 5.0	mW mW/°C
Total Power Dissipation @ $T_A = 25^{\circ}C$ Derate above $T_A = 25^{\circ}C$	P _D	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

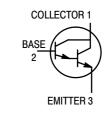
Characteristic	Symbol	Max	Unit	
Thermal Resistance, Junction-to-Ambient	R_{\thetaJA}	200	°C/W	
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



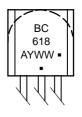
ON Semiconductor®

http://onsemi.com





MARKING DIAGRAM



= Assembly Location

= Year WW

А

Υ

= Work Week

= Pb-Free Package (Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
BC618G	TO-92 (Pb-Free)	5000 Units / Bulk
BC618RL1G	TO–92 (Pb–Free)	2000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	·				
Collector – Emitter Breakdown Voltage $(I_C = 10 \text{ mAdc}, V_{BE} = 0)$	V _{(BR)CEO}	55	_	_	Vdc
Collector-Base Breakdown Voltage $(I_C = 100 \ \mu Adc, I_E = 0)$	V _{(BR)CBO}	80	_	_	Vdc
Emitter – Base Breakdown Voltage $(I_E = 10 \ \mu Adc, I_C = 0)$	V _{(BR)EBO}	12	-	_	Vdc
Collector Cutoff Current ($V_{CE} = 60 \text{ Vdc}, V_{BE} = 0$)	ICES	_	-	50	nAdc
Collector Cutoff Current ($V_{CB} = 60 \text{ Vdc}, I_E = 0$)	I _{CBO}	_	-	50	nAdc
Emitter Cutoff Current ($V_{EB} = 10 \text{ Vdc}, I_C = 0$)	I _{EBO}	_	-	50	nAdc
ON CHARACTERISTICS					
DC Current Gain ($I_C = 200 \text{ mA}, I_B = 0.2 \text{ mA}$)	V _{CE(sat)}	_	-	1.1	Vdc
Base – Emitter Saturation Voltage ($I_C = 200 \text{ mA}, I_B = 0.2 \text{ mA}$)	V _{BE(sat)}	_	-	1.6	Vdc
$ \begin{array}{l} \text{DC Current Gain} \\ (I_{C} = 100 \ \mu\text{A}, \ V_{CE} = 5.0 \ \text{Vdc}) \\ (I_{C} = 10 \ \text{mA}, \ V_{CE} = 5.0 \ \text{Vdc}) \\ (I_{C} = 200 \ \text{mA}, \ V_{CE} = 5.0 \ \text{Vdc}) \\ (I_{C} = 1.0 \ \text{A}, \ V_{CE} = 5.0 \ \text{Vdc}) \end{array} $	h _{FE}	2000 4000 10000 4000	- - -	_ _ 50000 _	_
DYNAMIC CHARACTERISTICS					
Current–Gain – Bandwidth Product ($I_C = 500 \text{ mA}, V_{CE} = 5.0 \text{ Vdc}, P = 100 \text{ MHz}$)	f _T	150	_	_	MHz
Output Capacitance $(V_{CB} = 10 \text{ V}, I_E = 0, f = 1.0 \text{ MHz})$	C _{ob}	_	4.5	7.0	pF
Input Capacitance ($V_{EB} = 5.0 \text{ V}, I_E = 0, f = 1.0 \text{ MHz}$)	C _{ib}	_	5.0	9.0	pF

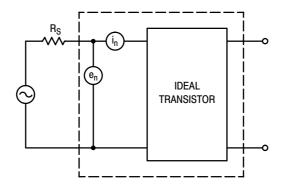
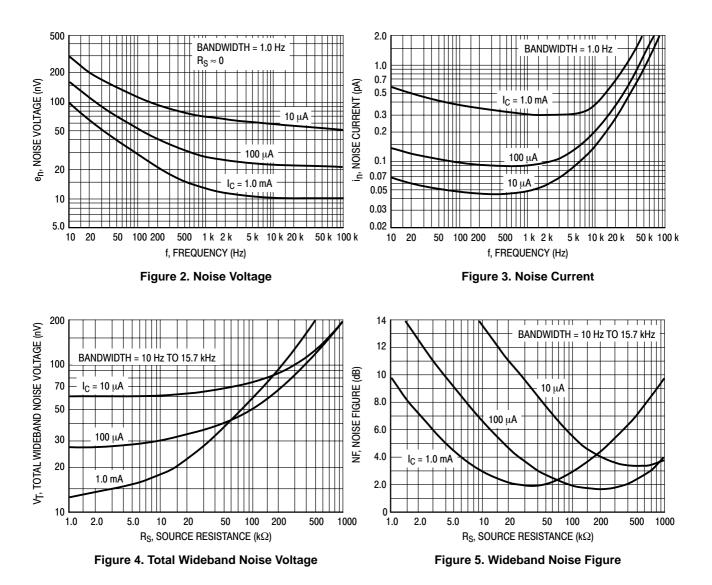


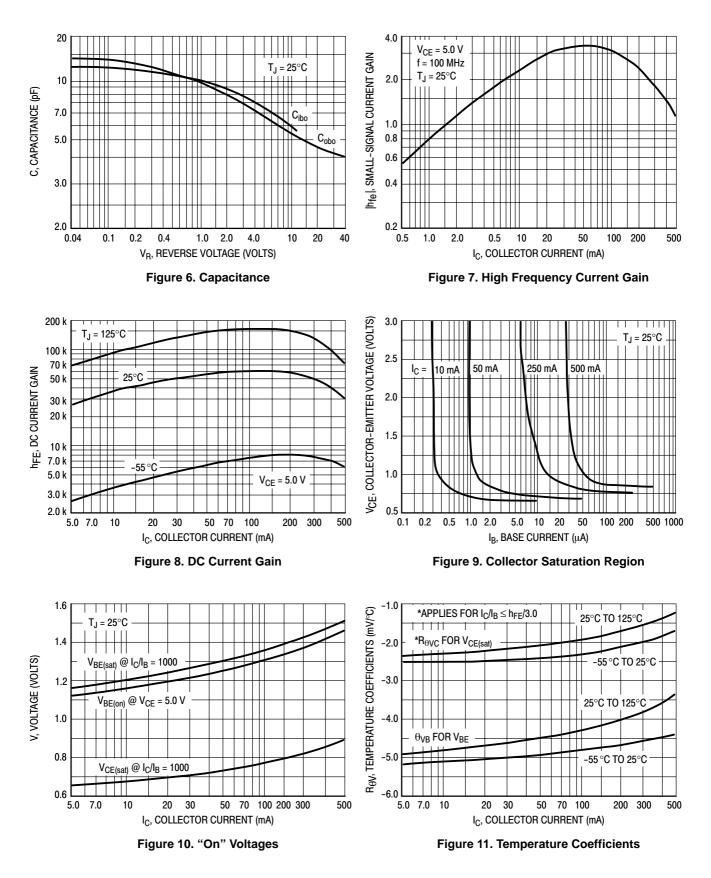
Figure 1. Transistor Noise Model

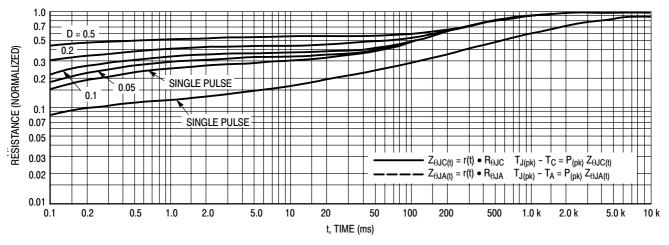
NOISE CHARACTERISTICS

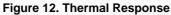
 $(V_{CE}=5.0~Vdc,~T_{A}=25^{\circ}C)$



SMALL-SIGNAL CHARACTERISTICS







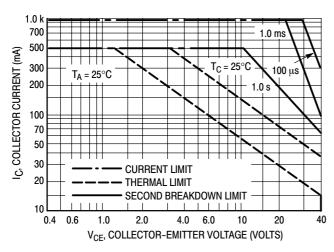
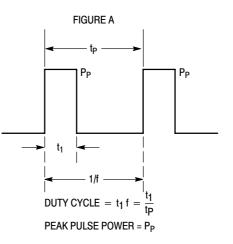


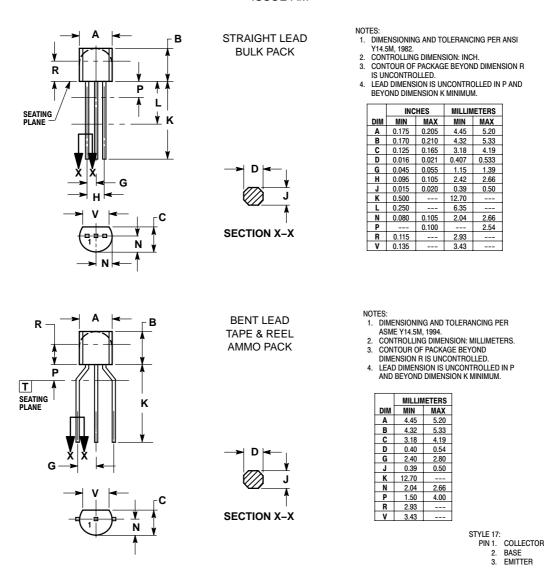
Figure 13. Active Region Safe Operating Area



Design Note: Use of Transient Thermal Resistance Data

PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUE AM



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