- Generates Either Odd or Even Parity for Nine Data Lines
- Cascadable for N-Bits Parity
- Package Options Include Plastic Small-Outline Packages, Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs

description

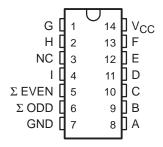
These universal, monolithic, 9-bit parity generators/checkers feature odd and even outputs to facilitate operation of either odd or even parity application. The word-length capability is easily expanded by cascading.

The SN54F280B is characterized for operation over the full military temperature range of -55°C to 125°C. The SN74F280B is characterized for operation from 0°C to 70°C.

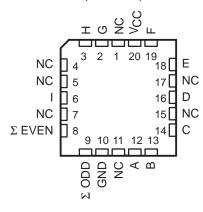
FUNCTION TABLE

NO. OF INPUTS	OUTPUTS					
THAT ARE HIGH	Σ EVEN	Σ ODD				
0, 2, 4, 6, 8	Н	L				
1, 3, 5, 7, 9	L	Н				

SN54F280B . . . J PACKAGE SN74F280B . . . D OR N PACKAGE (TOP VIEW)

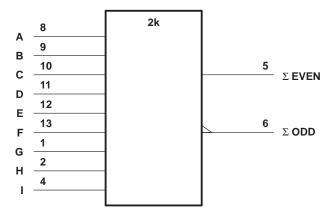


SN54F280B . . . FK PACKAGE (TOP VIEW)



NC - No internal connection

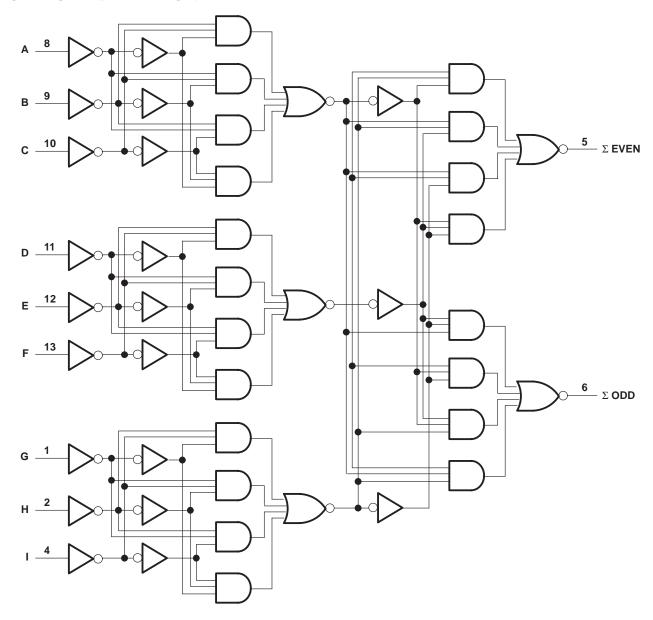
logic symbol†



[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12. Pin numbers shown are for the D, J, and N packages.



logic diagram (positive logic)



Pin numbers shown are for the D, J, and N packages.

SDFS008A - D2932, APRIL 1986 - REVISED OCTOBER 1993

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage range, V _{CC}	0.5 V to 7 V
Input voltage range (see Note 1)	1.2 V to 7 V
Input current range	30 mA to 5 mA
Voltage range applied to any output in the high state	0.5 V to V _{CC}
Current into any output in the low state	40 mA
Operating free-air temperature range: SN54F280B	. −55°C to 125°C
SN74F280B	0°C to 70°C
Storage temperature range	. −65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

		SN54F280B			SN74F280B			UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage	2			2			V
VIL	Low-level input voltage			0.8			0.8	V
ΙΙΚ	Input clamp current			-18			-18	mA
IOH	High-level output current			- 1			- 1	mA
loL	Low-level output current			20			20	mA
TA	Operating free-air temperature	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS		SI	SN54F280B			SN74F280B		
			MIN	TYP‡	MAX	MIN	TYP‡	MAX	UNIT
VIK	$V_{CC} = 4.5 \text{ V},$	$I_{I} = -18 \text{ mA}$			-1.2			-1.2	V
Vou	V _{CC} = 4.5 V	$I_{OH} = -1 \text{ mA}$	2.5	3.4		2.5	3.4		V
VOH	$V_{CC} = 4.75 \text{ V},$	$I_{OH} = -1 \text{ mA}$				2.7			
VOL	V _{CC} = 4.5 V	$I_{OL} = 20 \text{ mA}$		0.3	0.5		0.3	0.5	V
lį	$V_{CC} = 0$,	V _I = 7 V			0.1			0.1	mA
lН	$V_{CC} = 5.5 \text{ V},$	V _I = 2.7 V			20			20	μΑ
I _Ι Γ	V _{CC} = 5.5 V,	V _I = 0.5 V			- 20			- 20	μΑ
I _{OS} §	V _{CC} = 5.5 V,	VO = 0	-60		-150	-60		-150	mA
ICC	V _{CC} = 5.5 V,	V _I = 0		26	35		26	35	mA

[‡] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.



NOTE 1: The input voltage ratings may be exceeded provided the input current ratings are observed.

[§] Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.

SN54F280B, SN74F280B 9-BIT PARITY GENERATORS/CHECKERS

SDFS008A - D2932, APRIL 1986 - REVISED OCTOBER 1993

switching characteristics (see Note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC} = 5 \text{ V},$ $C_L = 50 \text{ pF},$ $R_L = 500 \Omega,$ $T_A = 25^{\circ}\text{C}$			V_{CC} = 4.5 V to 5.5 V, C_L = 50 pF, R_L = 500 Ω , T_A = MIN to MAX†				UNIT
			,	′F280B		SN54F280B		SN74F280B		
			MIN	TYP	MAX	MIN	MAX	MIN	MAX	
t _{PLH}	- Any input	Any input Σ EVEN	3.2	6.1	9	2.7	13	2.7	10	ns
^t PHL			3.2	6.6	10	2.7	15	2.7	11	115
t _{PLH}	Anytinnut	ΣODD	3.2	6.1	9	2.7	14	2.7	10	no
^t PHL	Any input		3.2	6.6	10	2.7	14	2.7	11	ns

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. NOTE 2: Load circuits and waveforms are shown in Section 1.



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