

Especially economical and universal operational amplifiers in plastic plug-in packages (14 pins) 20 A 14 DIN 41866, which by their excellent performance qualities are well suited for a wide range of applications. No external components for frequency compensation are required.

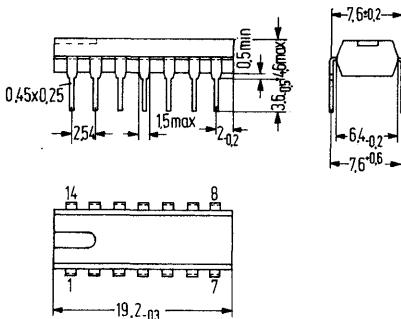
For single amplifier performance, see TAA 761 data sheet.

Additional features:

- Wide common-mode range
- Large supply voltage range
- Protection against destruction
- High output current
- Large control range
- No frequency compensation

Type	Ordering codes
TAA 4761 A	Q67000-A1032
TAA 4765 A	Q67000-A1033

Package outlines



Weight approx. 1.1 g
Plastic plug-in package 20 A 14 DIN 41866 (14 pins)
Dimensions in mm

Maximum ratings

- Supply voltage
- Output current
- Differential input voltage
- Junction temperature
- Storage temperature
- Thermal resistance system – ambient air

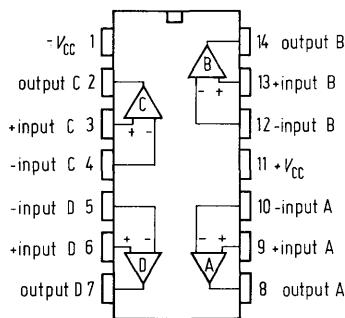
	TAA 4761 A	TAA 4765 A	
V_{CC}	±15		V
I_o	70		mA
V_{ID}	± V_{CC}		
T_J	150		°C
T_s	-55 to +125		°C
R_{thSam}	140		K/W

Range of operation

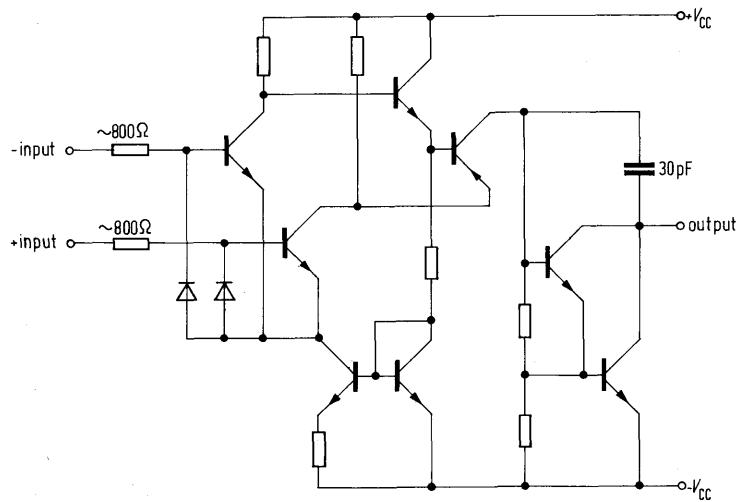
- Supply voltage
- Ambient temperature in operation (TAA 4761 A)
(TAA 4765 A)

V_{CC}	±2 to ±15	V
T_{amb}	0 to +70	°C
T_{amb}	-25 to +85	°C

Pin configuration



Circuit of one operational amplifier



Operating characteristics $(V_{cc} = \pm 15 \text{ V}; T_{amb} = 25^\circ\text{C})$

		TAA 4761 A	TAA 4765 A	
		min	typ	max
Supply current	I_{cc}		1	3
Input offset voltage ($R_G = 50 \Omega$)	V_{io}	-6	6	mA
Input offset current	I_{io}	-300	± 80	mV
Input current	I_i		.5	nA
Output voltage: $R_L = 2 \text{ k}\Omega$ $R_L = 620 \Omega$	V_{opp}	14.9	1.0	μA
Input impedance ($f = \text{kHz}$)	Z_i		-14	V
Open-loop voltage gain: $R_L = 2 \text{ k}\Omega, f = 100 \text{ Hz}$ $R_L = 10 \text{ k}\Omega, f = 100 \text{ Hz}$	G_v	80	-12.5	k Ω
Output leakage current	I_{olk}	200	dB	dB
Input common-mode range ($R_L = 2 \text{ k}\Omega$)	V_{icm}	85	90	dB
Common-mode rejection ratio ($R_L = 2 \text{ k}\Omega$)	$CMRR$	65	79	V
Sensitivity to supply voltage variations ($G_v = 100$)	$\frac{\Delta V_{io}}{\Delta V_{cc}}$	25	100	$\mu\text{V}/\text{V}$
Temp. coefficient of V_{io} ($R_G = 50 \Omega$)	α_{vio}	12	± 13.5	$\mu\text{V}/\text{K}$
Temp. coefficient of I_{io} ($R_G = 50 \Omega$)	α_{lio}	65	.3	nA/K
Noise voltage (to spec. DIN 45405, measured at input $R_s = 2.5 \text{ k}\Omega$)	V_N	3		μV
Output saturation voltage ($I_q = 10 \text{ mA}$) ($V_{cc} = \pm 5 \text{ V}$)	V_{qsat}		1	V
Supply current	I_{cc}		1	mA
Input offset voltage	V_{io}	-6	6	mV
Input offset current	I_{io}	-300	300	nA
Input current	I_i		1	μA
Output voltage ($R_L = 2 \text{ k}\Omega$)	V_{opp}	4.9	-4	V
Open-loop voltage gain ($R_L = 2 \text{ k}\Omega, f = 100 \text{ Hz}$)	G_v	70		dB