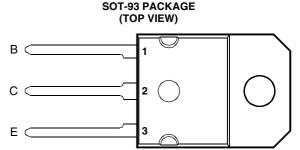
## **BOURNS®**

- Designed for Complementary Use with TIP145, TIP146 and TIP147
- 125 W at 25°C Case Temperature
- 10 A Continuous Collector Current
- Minimum h<sub>FE</sub> of 1000 at 4 V, 5 A



Pin 2 is in electrical contact with the mounting base.

MDTRAAA

## absolute maximum ratings at 25°C case temperature (unless otherwise noted)

RATING			VALUE	UNIT
	TIP140		60	
Collector-base voltage (I <sub>E</sub> = 0)	TIP141	V <sub>CBO</sub>	80	V
	TIP142		100	
	TIP140		60	
Collector-emitter voltage (I <sub>B</sub> = 0)	TIP141	V <sub>CEO</sub>	80	V
	TIP142		100	
Emitter-base voltage			5	V
Continuous collector current			10	Α
Peak collector current (see Note 1)			15	Α
Continuous base current			0.5	Α
Continuous device dissipation at (or below) 25°C case temperature (see Note 2)			125	W
Continuous device dissipation at (or below) 25°C free air temperature (see Note 3)			3.5	W
Unclamped inductive load energy (see Note 4)			100	mJ
Operating junction temperature range			-65 to +150	°C
Storage temperature range			-65 to +150	°C
Lead temperature 3.2 mm from case for 10 seconds			T <sub>L</sub> 260	

NOTES: 1. This value applies for  $t_p \leq 0.3$  ms, duty cycle  $\leq 10\%.$ 

- 2. Derate linearly to 150°C case temperature at the rate of 1 W/°C.
- 3. Derate linearly to  $150^{\circ}$ C free air temperature at the rate of 28 mW/°C.
- 4. This rating is based on the capability of the transistor to operate safely in a circuit of: L = 20 mH,  $I_{B(on)}$  = 5 mA,  $R_{BE}$  = 100  $\Omega$ ,  $V_{BE(off)}$  = 0,  $R_S$  = 0.1  $\Omega$ ,  $V_{CC}$  = 20 V.



## electrical characteristics at 25°C case temperature

	PARAMETER		TEST CONDITI	IONS	MIN	TYP	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage			TIP140	60			
		$I_C = 30 \text{ mA}$	$I_B = 0$	TIP141	80			V
		(see Note 5)		TIP142	100			
loeo.	Collector-emitter cut-off current	V <sub>CE</sub> = 30 V	I <sub>B</sub> = 0	TIP140			2	
		$V_{CE} = 40 \text{ V}$	$I_B = 0$	TIP141			2	mA
		$V_{CE} = 50 V$	$I_B = 0$	TIP142			2	
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> = 60 V	I <sub>E</sub> = 0	TIP140			1	
		$V_{CB} = 80 \text{ V}$	$I_E = 0$	TIP141			1	mA
		V <sub>CB</sub> = 100 V	$I_E = 0$	TIP142			1	
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> = 5 V	I <sub>C</sub> = 0				2	mA
h <sub>FE</sub>	Forward current	V <sub>CE</sub> = 4 V	I <sub>C</sub> = 5A	(see Notes 5 and 6)	1000			
	transfer ratio	V <sub>CE</sub> = 4 V	$I_{C} = 10 \text{ A}$		500			
V <sub>CE(sat)</sub>	Collector-emitter	I <sub>B</sub> = 10 mA	$I_C = 5 A$	(see Notes 5 and 6)			2	V
	saturation voltage	$I_B = 40 \text{ mA}$	$I_{\rm C} = 10 {\rm A}$				3	V
V <sub>BE</sub>	Base-emitter	V <sub>CE</sub> = 4 V	I <sub>C</sub> = 10 A	(see Notes 5 and 6)			3	V
	voltage						3	V
V <sub>EC</sub>	Parallel diode	I <sub>E</sub> = 10 A	I <sub>B</sub> = 0	(see Notes 5 and 6)			3.5	V
	forward voltage						0.5	V

NOTES: 5. These parameters must be measured using pulse techniques,  $t_0 = 300 \,\mu s$ , duty cycle  $\leq 2\%$ .

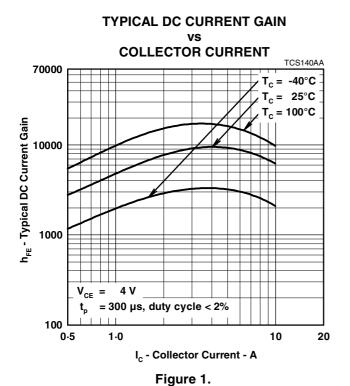
## resistive-load-switching characteristics at 25°C case temperature

	PARAMETER	TEST CONDITIONS †			MIN	TYP	MAX	UNIT
t <sub>on</sub>	Turn-on time	I <sub>C</sub> = 10 A	$I_{B(on)} = 40 \text{ mA}$	$I_{B(off)} = -40 \text{ mA}$		0.9		μs
t <sub>off</sub>	Turn-off time	$V_{BE(off)} = -4.2 \text{ V}$	$R_L = 3 \Omega$	$t_p = 20 \ \mu s, \ dc \le 2\%$		11		μs

<sup>&</sup>lt;sup>†</sup> Voltage and current values shown are nominal; exact values vary slightly with transistor parameters.

<sup>6.</sup> These parameters must be measured using voltage-sensing contacts, separate from the current carrying contacts.

## TYPICAL CHARACTERISTICS



**COLLECTOR-EMITTER SATURATION VOLTAGE** 

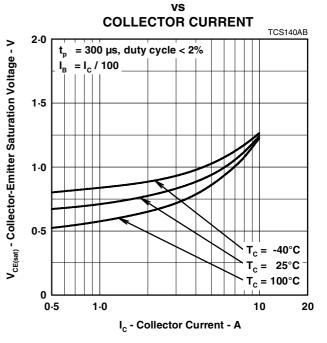


Figure 2.

## **BASE-EMITTER SATURATION VOLTAGE**

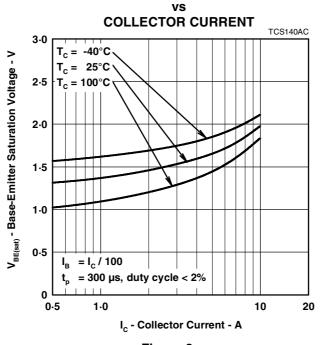
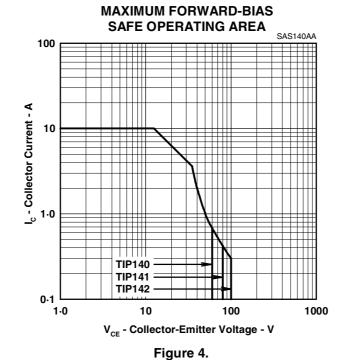


Figure 3.

## **MAXIMUM SAFE OPERATING REGIONS**



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# THERMAL INFORMATION

## **MAXIMUM POWER DISSIPATION** vs **CASE TEMPERATURE** TIS140AA 140 P<sub>tot</sub> - Maximum Power Dissipation - W 120 100 80 60 40 20 0 0 25 50 75 100 125 150 T<sub>C</sub> - Case Temperature - °C

Figure 5.

PRODUCT INFORMATION

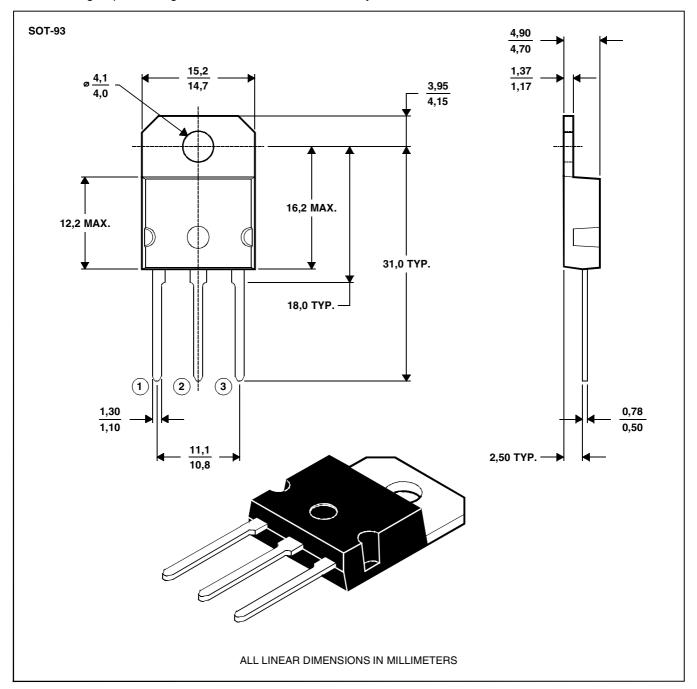


#### **MECHANICAL DATA**

## **SOT-93**

## 3-pin plastic flange-mount package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.



NOTE A: The centre pin is in electrical contact with the mounting tab.

**MDXXAW**