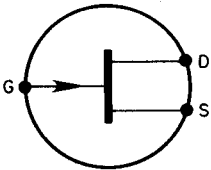
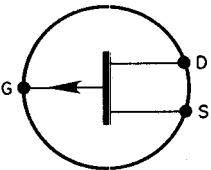
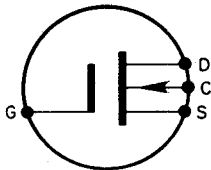
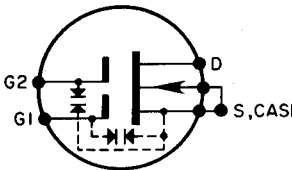
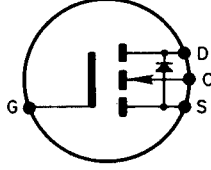
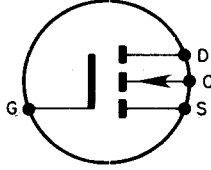
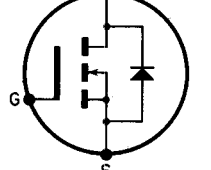
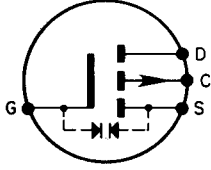
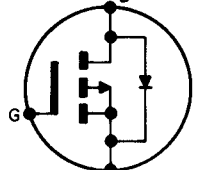


FET and MOSFET Diagrams (Observe MOS Handling) ▲

<p>Depletion N-Ch JFET</p>  <p>ECG 312 456 451 457 461 (Dual) 468 452 458 466 469 453 459 467</p>	<p>Depletion P-Ch JFET</p>  <p>ECG 326 460 489</p>	<p>Depletion N-Ch MOSFET</p>  <p>ECG 220 462</p>	<p>Dual Gate Depletion N-Ch MOSFET</p>  <p>ECG 221 222 (Gate Protected) 454 (Gate Protected) 455 (Gate Protected)</p>
<p>Enhancement N-Ch MOSFET</p>  <p>ECG 2384 2386 2392</p>	<p>Enhancement N-Ch MOSFET</p>  <p>ECG 465</p>	<p>Enhancement N-Ch MOSFET</p>  <p>ECG 66 2380 2390 2397 67 2382 2391 2398 490 2385 2393 2399 2375 2387 2394 2376 2388 2395 2377 2389 2396</p>	<p>Enhancement P-Ch MOSFET</p>  <p>ECG 463 (Dual) (Gate Protected) 464</p>
<p>Enhancement P-Ch MOSFET</p>  <p>ECG 2381 2382</p>			

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