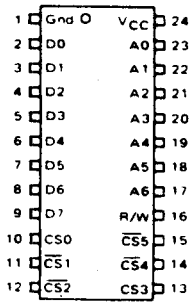
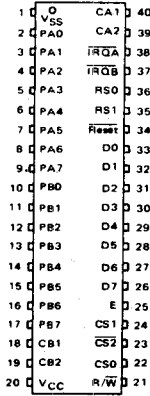


Microprocessor and Memory Circuits (cont'd)

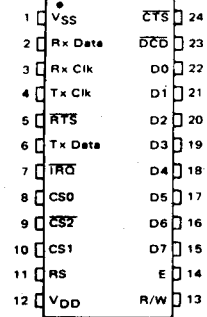
ECG6810 24-Pin DIP See Fig. D15
 NMOS 1K Static RAM (SRAM), Organized
 128 Words by 8 Bits, 450 nsec Max Access
 Time, $V_{CC} = +5V$



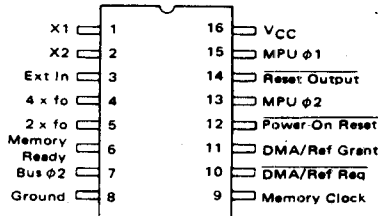
ECG6821 40-Pin DIP See Fig. D19
 NMOS Peripheral Interface Adapter (PIA),
 1 MHz Max Clock Rate,
 $V_{CC} = +5V$



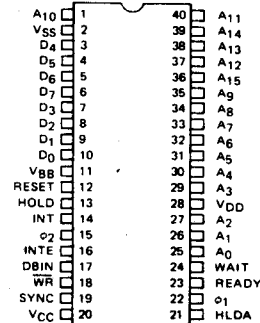
ECG6850 24-Pin DIP See Fig. D15
 NMOS Asynchronous Communications Inter-
 face Adapter (ACIA), 1 MHz Max Clock
 Rate, $V_{CC} = +5V$



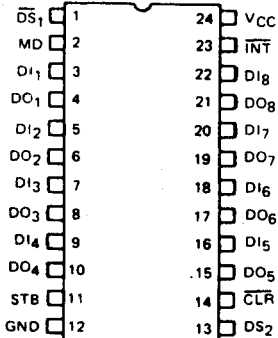
ECG6875 16-Pin DIP See Fig. D8
 Schottky Two-Phase Clock Generator/Driver, $V_{CC} = +5V$



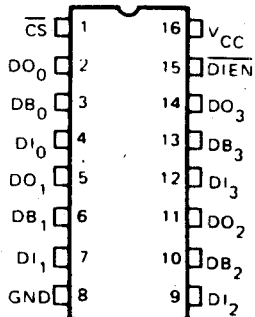
ECG8080A 40-Pin DIP See Fig. D19
 NMOS 8-Bit Microprocessor (MPU), 2 MHz Max Clock Rate,
 $V_{CC} = +5V$, $V_{BB} = -5V$, $V_{DD} = +12V$



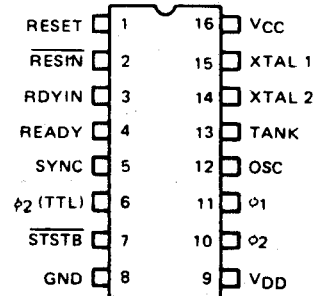
ECG8212 24-Pin DIP See Fig. D15
 Schottky 8-Bit Input/Output Port,
 $V_{CC} = +5V$



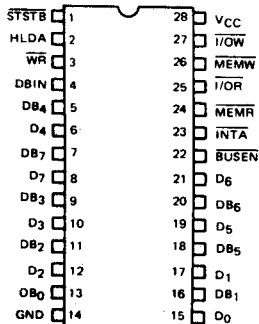
ECG8216 16-Pin DIP See Fig. D8
 Schottky 4-Bit Parallel Bidirectional Non-
 Inverting Bus Driver, $V_{CC} = +5V$



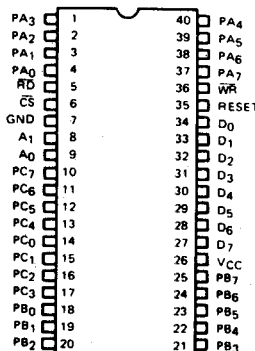
ECG8224 16-Pin DIP See Fig. D8
 Schottky Clock Generator/Driver for 8080A
 MPU Systems, $V_{CC} = +5V$



ECG8228 28-Pin DIP See Fig. D17
 Schottky System Controller and Bus Driver
 for 8080A MPU Systems, $V_{CC} = +5V$



ECG8255 40-Pin DIP See Fig. D19
 NMOS Programmable Peripheral Interface,
 $V_{CC} = +5V$



ECG65101 22-Pin DIP See Fig. D13
 CMOS Static 1K RAM, Organized 256
 Words by 4 Bits, 450 nsec Max Access
 Time, $V_{CC} = +5V$

