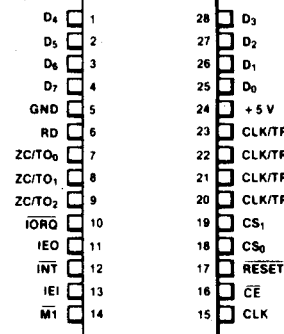
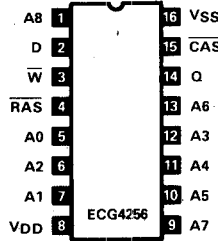


Microprocessor and Memory Circuits (cont'd)

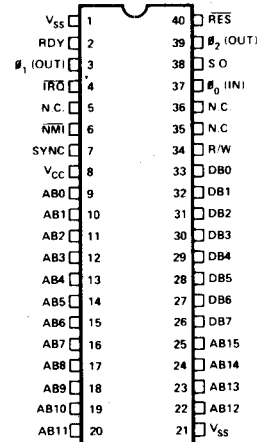
ECG3882 28-Pin Dip See Fig. D17
NMOS Programmable Counter
Timer Circuit (CTC), $V_{CC} = +5 V$



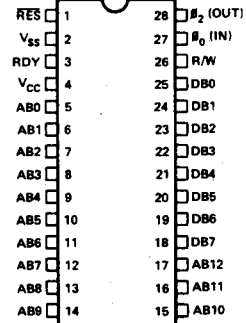
ECG4256 16-Pin DIP See Fig. D8
NMOS - 256K DRAM, 100 nsec
Access Time, 256K x 1, Page
Mode, $V_{DD} = +5 V$



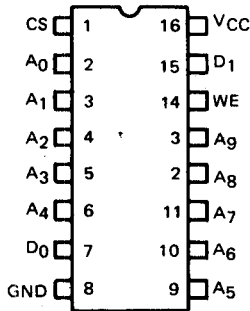
ECG6502 40-Pin DIP See Fig. D19
NMOS 8-Bit Microprocessor
(MPU), 1 MHz Max Clock Rate,
 $V_{CC} = +5 V$



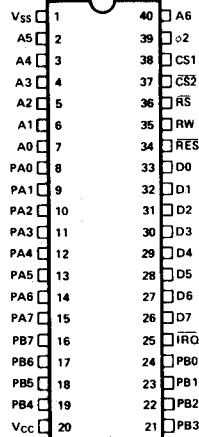
ECG6507 28-Pin DIP See Fig. D18
NMOS 8-Bit Microprocessor
(MPU) with Internal Clock Osc/
Driver, 1 MHz Max Clock Rate,
 $V_{CC} = +5 V$



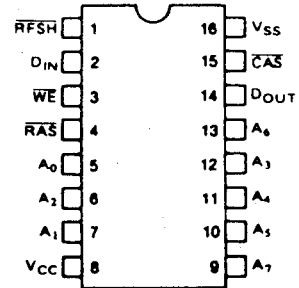
ECG6508 16-Pin DIP See Fig. D8
CMOS 1K Static RAM (SRAM), Organized
1K by 1 Bit, 300 nsec Max Access Time,
 $V_{CC} = +5 V$



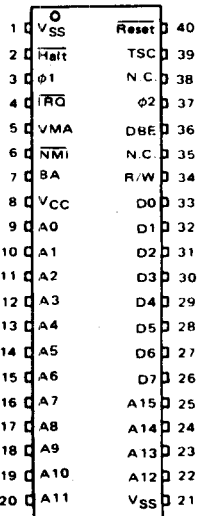
ECG6532 40-Pin DIP See Fig. D19
NMOS Combination 1K Static RAM (128
Words by 8 Bits), I/O and Timer Array,
 $V_{CC} = +5 V$



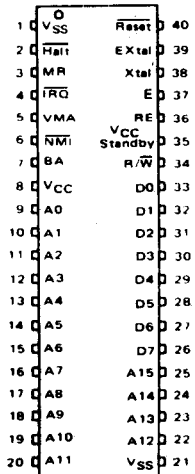
ECG6664 16-Pin DIP See Fig. D8
NMOS 64K Dynamic RAM (DRAM) with
Pin #1 Refresh Control, Organized 64K by
1 Bit, 150 nsec Max Access Time,
 $V_{CC} = +5 V$



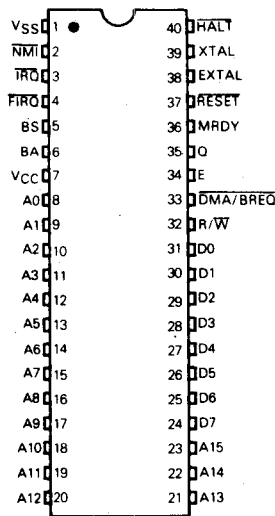
ECG6800 40-Pin DIP See Fig. D19
NMOS 8-Bit Microprocessor
(MPU), 1 MHz Max Clock Rate,
 $V_{CC} = +5 V$



ECG6802 40-Pin DIP See Fig. D19
NMOS 8-Bit Microprocessor
(MPU) with Internal Clock Osc/
Driver and 128 Bytes of Static
RAM, 1 MHz Max Clock Rate,
 $V_{CC} = +5 V$



ECG6809 40-Pin DIP See Fig. D19
NMOS 8-Bit Microprocessor
(MPU) with Internal Clock Osc/
Driver, 1 MHz Max Clock Rate



ECG6809E 40-Pin DIP See Fig. D19
NMOS 8-Bit Microprocessor
(MPU) Requires External Clock,
1 MHz Max Clock Rate,
 $V_{CC} = +5 V$

