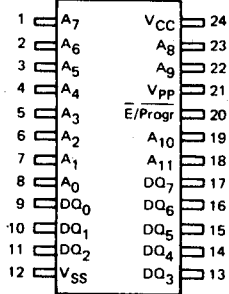
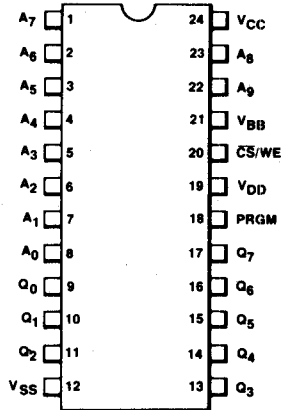


Microprocessor and Memory Circuits (cont'd)

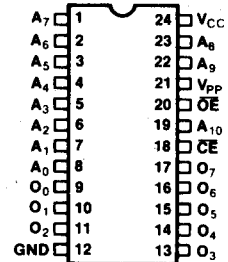
ECG2532 24-Pin DIP See Fig. D16
 NMOS 32K UV EPROM, Organized 4K by 8 Bits, 300 nsec Max Access Time. Single Supply: $V_{CC} = +5V$, $V_{PP} = +25V$



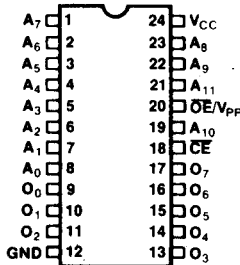
ECG2708 24-Pin DIP See Fig. D16
 NMOS 8K UV EPROM, Organized 1K by 8 Bits, 450 nsec Max Access Time. Triple Supply: $V_{CC} = +5V$, $V_{BB} = -5V$, $V_{DD} = +12V$, $V_{SS} = GND$



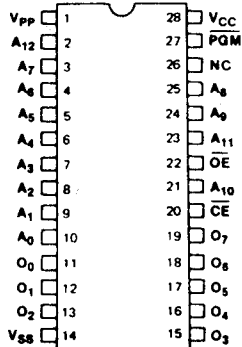
ECG2716 24-Pin DIP See Fig. D16
 NMOS 16K UV EPROM, Organized 2K by 8 Bits, 450 nsec Max Access Time. Single Supply: $V_{CC} = +5V$, $V_{PP} = +25V$



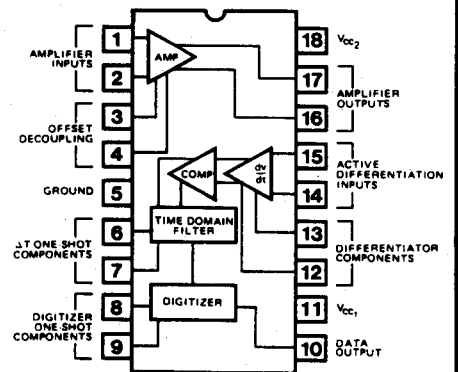
ECG2732 24-Pin DIP See Fig. D16
 NMOS 32K UV EPROM, Organized 4K by 8 Bits, 200 nsec Max Access Time. Single Supply: $V_{CC} = +5V$, $V_{PP} = +21V$



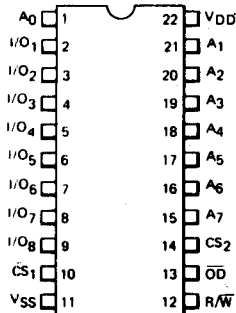
ECG2764 28-Pin DIP See Fig. D18-1
 NMOS 64K EPROM, Organized 8K by 8 Bits, 200 nsec Max Access Time
 $V_{CC} = +5V$, $V_{PP} = +12V$



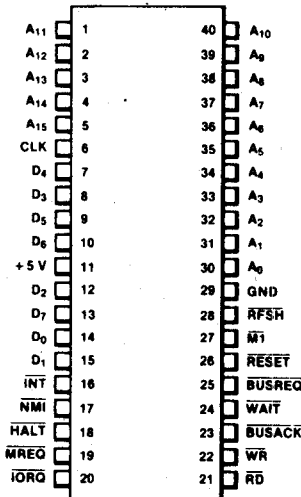
ECG3470 18-Pin DIP See Fig. D11
 Floppy Disk Read Amplifier System
 $V_{CC1} = +5V$, $V_{CC2} = +10V$ to $+14V$



ECG3539 22-Pin DIP See Fig. D13
 NMOS 2K Static RAM (SRAM), Organized 256 Words by 8 Bits, 500 nsec Max Access Time. Single Supply: $V_{CC} = +5V$



ECG3880 40-Pin DIP See Fig. D19
 NMOS 8-Bit Microprocessor (MPU), 4 MHz Max Clock Rate, $V_{CC} = +5V$



ECG3881 40-Pin DIP See Fig. D19
 NMOS Parallel I/O Interface Adapter (PIO), 4 MHz Max Clock Rate, $V_{CC} = +5V$

