

# Display Driver Circuits (cont'd)

**ECG** **10 Max\*** 16-Pin DIP  
**2087** 1.5 A See Fig. D8  
**2088** 1.5 A

4 Unit Darlington Array/Driver, w/Pre-Driver

**ECG** **10 Max\*** 16-Pin DIP  
**2090** 150 mA See Fig. D8

7 Unit Transistor Array/Driver, w/Clamp Diode

**ECG** 16-Pin DIP See Fig. D9  
**2631**

$V_{CC} = +5 V$

Quad EIA-422 Line Driver

**ECG** 16-Pin DIP See Fig. D9  
**2632**

$V_{CC} = +5 V$

Quad EIA-422/3 Line Receiver

**ECG** 16-Pin DIP See Fig. D8  
**6880**

$V_{CC} = +5 V$

Schottky Quad 3-State Bus Transceiver (Inverting)

**ECG** 16-Pin DIP See Fig. D8  
**6885**

$V_{CC} = +5 V$

Schottky Hex 3-State Bus/IF Driver with Common Enable (Non-Inverting)

**ECG** 16-Pin DIP See Fig. D8  
**6886**

$V_{CC} = +5 V$

Schottky Hex 3-State Bus/IF Driver, with Common Enable (Inverting)

**ECG** 16-Pin DIP See Fig. D8  
**6887**

$V_{CC} = +5 V$

Schottky Hex 3-State Bus/IF Driver, w/2 & 4 Line Enable (Non-Inverting)

**ECG** 16-Pin DIP See Fig. D8  
**6888**

$V_{CC} = +5 V$

Schottky Hex 3-State Bus/IF Driver, w/2 & 4 Line Enable (Inverting)

**ECG** 16-Pin DIP See Fig. D8  
**6889**

$V_{CC} = +5 V$

Schottky Quad 3-State Bus Transceiver (Non-Inverting)

**ECG** 14-Pin DIP See Fig. D6  
**75188**

$V_{CC} = +9 V$   
 $V_{EE} = -9 V$

DTL Quad Line Driver

**ECG** 14-Pin DIP See Fig. D6  
**75189**

$V_{CC} = +5 V$

DTL Quad Line Receiver

\*  $I_0 \text{ Max}$  = Max Current Per Output

Package Outlines - See Page 1-357