

# Display Driver Circuits

<p><b>ECG</b> <b><math>I_o</math> Max*</b></p> <p>2011 500 mA 2012 500 mA 2013 500 mA 2014 500 mA 2015 500 mA 2074 400 mA 2076 150 mA 2081 100 mA</p> <p>16-Pin DIP See Fig. D8</p> <p>7 Unit Darlington Array/Driver</p>	<p><b>ECG</b> <b><math>I_o</math> Max*</b></p> <p>2016 500 mA 2017 500 mA 2018 500 mA 2019 500 mA 2020 500 mA</p> <p>18-Pin DIP See Fig. D10</p> <p>8 Unit Darlington Array/Driver</p>	
<p><b>ECG</b> <b><math>I_o</math> Max*</b></p> <p>2021 40 mA 2022 40 mA</p> <p><math>V_{BB} = +80 V</math></p> <p>18-Pin DIP See Fig. D10</p> <p>8 Digit/Segment, Darlington Array/Driver</p>	<p><b>ECG</b> <b><math>I_o</math> Max*</b></p> <p>2023 200 mA</p> <p>16-Pin DIP See Fig. D8</p> <p>7 Segment/LED, Transistor Array/Driver</p>	<p><b>ECG</b> <b><math>I_o</math> Max*</b></p> <p>2024 25 mA</p> <p><math>V_{CC} = +5 V</math></p> <p>24-Pin DIP See Fig. D15</p> <p>2 Digit BCD-to-7 Segment Decoder/Driver</p>
<p><b>ECG</b> <b><math>I_o</math> Max*</b></p> <p>2025 40 mA</p> <p><math>V_{BB} = +100 V</math></p> <p>14-Pin DIP See Fig. D6</p> <p>4 HV Digit Driver</p>	<p><b>ECG</b> <b><math>I_o</math> Max*</b></p> <p>2026 40 mA</p> <p><math>V_{BB} = +100 V</math></p> <p>16-Pin DIP See Fig. D8</p> <p>6 HV Digit Driver</p>	<p><b>ECG</b> <b><math>I_o</math> Max*</b></p> <p>2027 350 mA</p> <p><math>V_{SS} = +10 V</math></p> <p>14-Pin DIP See Fig. D6</p> <p>6 Digit Driver</p>
<p><b>ECG</b> <b><math>I_o</math> Max*</b></p> <p>2028 1.5 mA</p> <p><math>V_{CC} = +5 V</math></p> <p>16-Pin DIP See Fig. D8</p> <p>BCD-to-7 Segment Decoder/Driver</p>	<p><b>ECG</b> <b><math>I_o</math> Max*</b></p> <p>2029 50 mA</p> <p><math>V_{EE} = -55 V</math> <math>V_{CC} = GND</math></p> <p>16-Pin DIP See Fig. D8</p> <p>7 HV Digit Driver (Active High Input)</p>	<p><b>ECG</b></p> <p>2030</p> <p>Programming Current</p> <p>18-Pin DIP See Fig. D10</p> <p>8 HV Segment Driver</p>

\*  $I_o$  Max = Max Current Per Output

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