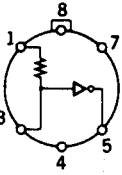
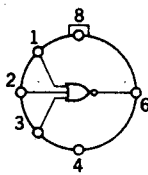
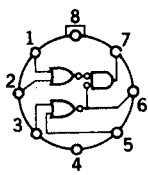
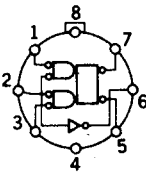
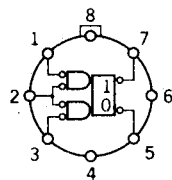
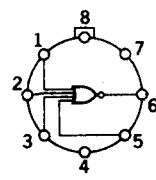
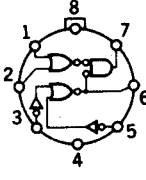
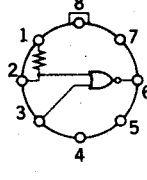
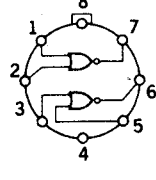
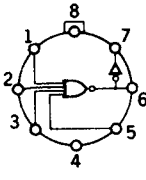
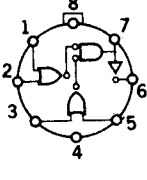
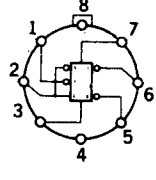
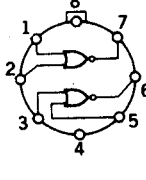
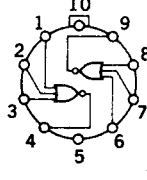
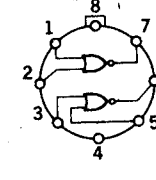


RTL Resistor-Transistor Logic

<p>ECG9900 8-Pin Can See Fig. D2 Buffer-Medium Power, $V_{CC} = +3.6$ V (Nom.)</p>  <p>$V_{CC} = \text{Pin 8, GND} = \text{Pin 4}$</p>	<p>ECG9903 8-Pin Can See Fig. D2 3-Input NOR Gate-Medium Power, $V_{CC} = +3.6$ V (Nom.)</p>  <p>$V_{CC} = \text{Pin 8, GND} = \text{Pin 4}$</p>	<p>ECG9904 8-Pin Can See Fig. D2 Half Adder-Medium Power, $V_{CC} = +3.6$ V (Nom.)</p>  <p>$V_{CC} = \text{Pin 8, GND} = \text{Pin 4}$</p>
<p>ECG9905 8-Pin Can See Fig. D2 Half-Shift Register-Medium Power, $V_{CC} = +3.6$ V (Nom.)</p>  <p>$V_{CC} = \text{Pin 8, GND} = \text{Pin 4}$</p>	<p>ECG9906 8-Pin Can See Fig. D2 Half-Shift Register W I/O-Inverter-Medium Power, $V_{CC} = +3.6$ V (Nom.)</p>  <p>$V_{CC} = \text{Pin 8, GND} = \text{Pin 4}$</p>	<p>ECG9907 8-Pin Can See Fig. D2 4-Input NOR Gate-Medium Power, $V_{CC} = +3.6$ V (Nom.)</p>  <p>$V_{CC} = \text{Pin 8, GND} = \text{Pin 4}$</p>
<p>ECG9908 8-Pin Can See Fig. D2 Adder and Exclusive OR-Low Power, $V_{CC} = +3.6$ V (Nom.)</p>  <p>$V_{CC} = \text{Pin 8, GND} = \text{Pin 4}$</p>	<p>ECG9909 8-Pin Can See Fig. D2 Buffer-Low Power, $V_{CC} = +3.6$ V (Nom.)</p>  <p>$V_{CC} = \text{Pin 8, GND} = \text{Pin 4}$</p>	<p>ECG9910 8-Pin Can See Fig. D2 Dual 2-Input NOR Gate-Low Power, $V_{CC} = +3.6$ V (Nom.)</p>  <p>$V_{CC} = \text{Pin 8, GND} = \text{Pin 4}$</p>
<p>ECG9911 8-Pin Can See Fig. D2 4-Input NOR Gate and Inverter-Low Power, $V_{CC} = +3.6$ V (Nom.)</p>  <p>$V_{CC} = \text{Pin 8, GND} = \text{Pin 4}$</p>	<p>ECG9912 8-Pin Can See Fig. D2 Half Adder-Low Power, $V_{CC} = +3.6$ V (Nom.)</p>  <p>$V_{CC} = \text{Pin 8, GND} = \text{Pin 4}$</p>	<p>ECG9913 8-Pin Can See Fig. D2 Type "D" Flip-Flop-Low Power, $V_{CC} = +3.6$ V (Nom.)</p>  <p>$V_{CC} = \text{Pin 8, GND} = \text{Pin 4}$</p>
<p>ECG9914 8-Pin Can See Fig. D2 Dual 2-Input NOR Gate-Medium Power, $V_{CC} = +3.6$ V (Nom.)</p>  <p>$V_{CC} = \text{Pin 8, GND} = \text{Pin 4}$</p>	<p>ECG9915 10-Pin Can See Fig. D3 Dual 3-Input NOR Gate-Medium Power, $V_{CC} = +3.6$ V (Nom.)</p>  <p>$V_{CC} = \text{Pin 10, GND} = \text{Pin 5}$</p>	<p>ECG9921 8-Pin Can See Fig. D2 Gate Expander-Low Power, $V_{CC} = +3.6$ V (Nom.)</p>  <p>$V_{CC} = \text{Pin 8, GND} = \text{Pin 4}$</p>