

# HTL (cont'd)

**ECG9684** 16-Pin DIP See Fig. D8  
Decade Counter,  $V_{CC} = +15\text{ V (Nom.)}$

**ECG9685\***  
Binary Counter,  $V_{CC} = +15\text{ V (Nom.)}$

\* Discontinued

$V_{CC} = \text{Pin 16, GND} = \text{Pin 8}$

**ECG9686** 16-Pin DIP See Fig. D8  
4-Bit Shift Register,  $V_{CC} = +15\text{ V (Nom.)}$

$V_{CC} = \text{Pin 16, GND} = \text{Pin 8}$

**ECG9688** 16-Pin DIP See Fig. D8  
Dual J-K Flip-Flop,  $V_{CC} = +15\text{ V (Nom.)}$

$V_{CC} = \text{Pin 16, GND} = \text{Pin 8}$

**ECG9689** 14-Pin DIP See Fig. D6  
Hex Inverter (High Voltage),  
 $V_{CC} = +15\text{ V (Nom.)}$

**ECG9690**  
Hex Inverter (Active Pull-Up),  
 $V_{CC} = +15\text{ V (Nom.)}$

**ECG9691**  
Hex Inverter/Interface Element,  
 $V_{CC} = +15\text{ V (Nom.)}$

$V_{CC} = \text{Pin 14, GND} = \text{Pin 7}$

**ECG9696** 16-Pin DIP See Fig. D8  
Dual Interface Element, Line Driver/  
Receiver,  $V_{CC} = +15\text{ V (Nom.)}$

$V_{CC} = \text{Pin 16, GND} = \text{Pin 8}$

Package Outlines - See Page 1-357

