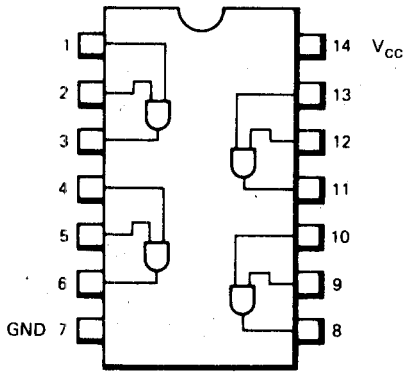
DTL (cont'd)

ECG9806 14-Pin DIP See Fig. D6
Quad 2-Input AND Gate (6 Kohm-Pullup)

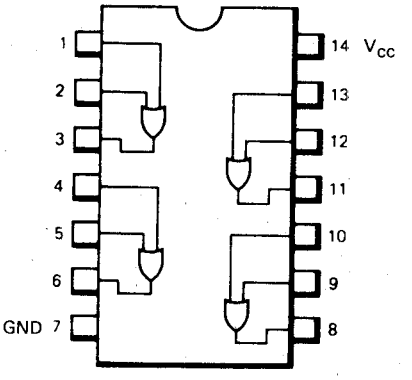
ECG9807
Quad 2-Input AND Gate (2 Kohm-Pullup)



V_{CC} = +5 V (Nom.)

ECG9808 14-Pin DIP See Fig. D6
Quad 2-Input OR Gate (6 Kohm-Pullup)

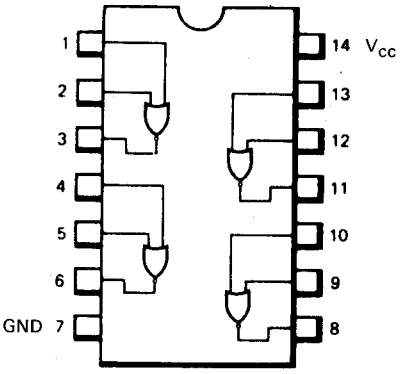
ECG9809
Quad 2-Input OR Gate (2 Kohm-Pullup)



V_{CC} = +5 V (Nom.)

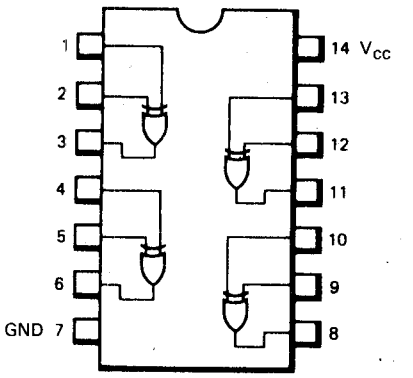
ECG9810 14-Pin DIP See Fig. D6
Quad 2-Input NOR Gate (6 Kohm-Pullup)

ECG9811
Quad 2-Input NOR Gate (2 Kohm-Pullup)



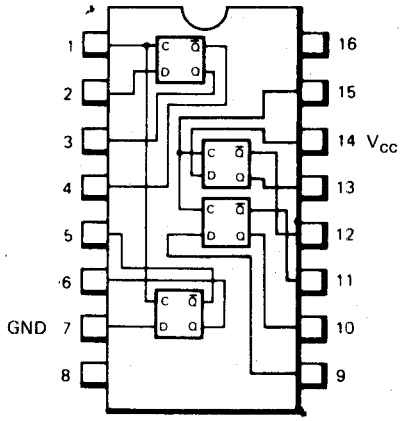
V_{CC} = +5 V (Nom.)

ECG9812 14-Pin DIP See Fig. D6
Quad 2-Input Exclusive OR Gate



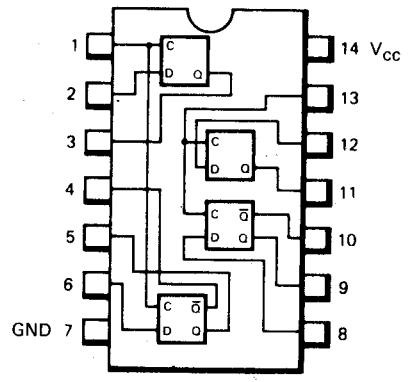
V_{CC} = +5 V (Nom.)

ECG9813 16-Pin DIP See Fig. D8
Quad Latch with Complementary Outputs



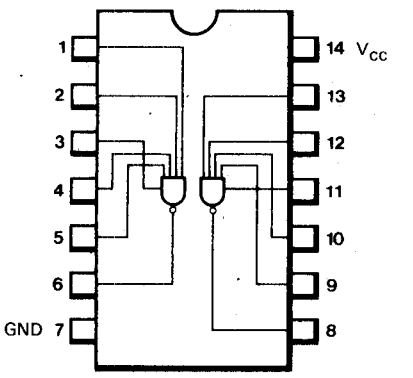
V_{CC} = +5 V (Nom.)

ECG9814 14-Pin DIP See Fig. D6
Quad Latch



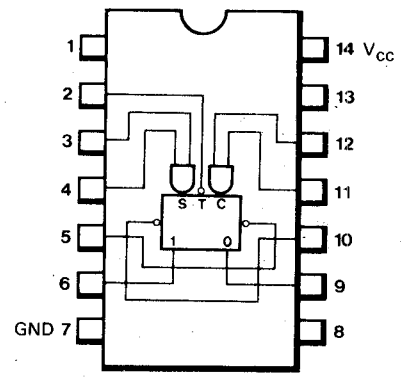
V_{CC} = +5 V (Nom.)

ECG9930 14-Pin DIP See Fig. D6
Dual 4-Input Extendable NAND Gate



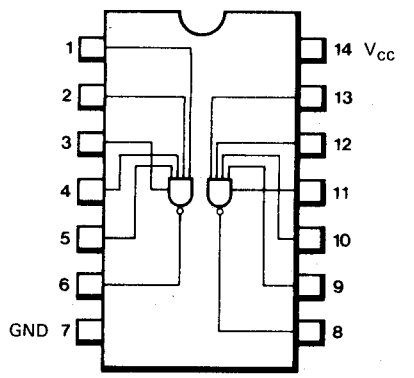
V_{CC} = +5 V (Nom.)

ECG9931 14-Pin DIP See Fig. D6
RS Clocked Flip-Flop



V_{CC} = +5 V (Nom.)

ECG9932 14-Pin DIP See Fig. D6
Dual 4-Input Extendable NAND Buffer Gate



V_{CC} = +5 V (Nom.)

Package Outlines - See Page 1-357