

# Linear IC and Module Circuits (cont'd)

**ECG1800** 30-Pin DIP See Fig. L124C  
TV Sound MPX Decoder,  $V_{CC} = 12\text{ V Typ}$

GND	1	30	SAP LAMP
LIM AMP INPUT	2	29	STEREO LAMP
FILTER CAP	3	28	PHASE COMP CAP
FILTER CAP	4	27	PHASE COMP CAP
5 FH VCO CAP	5	26	PHASE COMP INPUT
5 FH ADJ	6	25	PHASE COMP
PHASE DET CAP	7	24	PHASE COMP
L-R SAP OUTPUT	8	23	4 FH ADJ
MODE SW	9	22	PILOT REGEN CAP
MONO SW	10	21	PILOT INPUT
L-R SAP INPUT	11	20	STEREO INPUT
L-R INPUT	12	19	PRE-AMP OUTPUT
R OUTPUT	13	18	PRE-AMP INPUT
L OUTPUT	14	17	MUTE
$V_{CC} 12\text{V}$	15	16	L-R OUTPUT

**ECG1801** 28-Pin DIP See Fig. L123A  
TV dbx Decoder/Voltage Controlled Amp/  
RMS Level Detector,  $V_{CC} = 12\text{ V Typ}$

GND	1	28	REF VOLTAGE
NON-INV INPUT	2	27	CAPACITOR
INV INPUT	3	26	BUFFER INPUT
PRE-AMP OUTPUT	4	25	BUFFER OUTPUT
BUFFER INPUT	5	24	VCA1 INPUT
BUFFER OUTPUT	6	23	VCA1 ADJ
RMS DET 1 INPUT	7	22	NC
CAPACITOR	8	21	NC
RMS DET 2 INPUT	9	20	AMP 3 INPUT
CAPACITOR	10	19	AMP 3 OUTPUT
RESISTOR ADJ	11	18	VCA-2 INPUT
NON-INV INPUT	12	17	AMP 4 INPUT
INV INPUT	13	16	L-R/SAP OUTPUT
$V_{CC} 12\text{V}$	14	15	L+R OUTPUT

**ECG1802** 13-Lead Formed SIP See Fig. L57B  
Dual 6 W/Ch, 22 W (BTL) AF PO,  
 $V_{CC} = 14\text{ V}$ ,  $R_L = 4\ \Omega$

NON-INV INPUT 1	1	2	INV INPUT
GND SIGNAL	3	4	V REF
OUTPUT 1	5	6	BOOTSTRAP 1
GND SUBSTRATE	7	8	BOOTSTRAP 2
OUTPUT 2	9	10	$V_{CC} 14\text{V}$
MUTE/SB SW	11	12	FILTER CAP
NON-INV INPUT 2	13		

**ECG1803** 18-Pin DIP See Fig. L115  
Dual Tone/Volume Control,  $V_{CC} = 12\text{ V Typ}$

VOL CTRL INPUT	1	18	GND
FILTER CAP	2	17	CONTOUR SW
$V_{CC} 12\text{V}$	3	16	BALANCE CTRL INPUT
R INPUT	4	15	L INPUT
COUPLING CAP	5	14	COUPLING CAP
CAPACITOR	6	13	CAPACITOR
R OUTPUT	7	12	CAPACITOR
BASS CTRL INPUT	9	11	L OUTPUT
		10	TREBLE CTRL INPUT

**ECG1804** 13-Lead Formed SIP See Fig. L57B  
Vertical Deflection Output,  $V_{CC} = 26\text{ V Typ}$

OSC ADJ	1	13	OSC CAP
SYNC IN/BLANK OUT	2		
SAWTOOTH GEN OUT	3		
PRE-AMP INPUT	4		
VERT OUT $V_{CC}$	5		
VERT OUT	6		
FB GEN OUTPUT	7		
GND	8		
$V_{CC} 26\text{V}$	9		
VREF	10		
SAWTOOTH GEN CAP	11		
50/60 HZ DET OUT	12		

**ECG1805** 28-Pin DIP See Fig. L123A  
VCR Video Processor,  $V_{CC} = 5\text{ V Typ}$

SYNC SEP	1	28	FM OUT
SYNC OUT	2	27	WHITE CLIP ADJ
EE LEVEL ADJ	3	26	MOD
REC VID IN	4	25	MOD
AGC DET	5	24	DARK CLIP ADJ
PB VID IN	6	23	MOD IN
SUB CLAMP DET	7	22	FB AMP IN
SUB CLAMP OUT	8	21	EMPHASIS OUT
GND	9	20	$V_{CC}$
EE/VV OUT	10	19	CLAMP IN
SYNC PULSE IN	11	18	PG IN & HEAD SELECT
PB CHROMA IN	12	17	NL EMPHASIS IN
LPF NOISE CAN IN	13	16	12 DB AMP OUT
HPF NOISE CAN IN	14	15	12 DB AMP IN

**ECG1806** 22-Pin DIP See Fig. L121A  
VCR 4 Head Amp,  $V_{CC} = 5\text{ V Typ}$

HEAD SW	1	22	AGC DET
INITIAL BIAS CH 1	2	21	AGC REVERSE OUTPUT
INITIAL INPUT CH 1	3	20	AGC OUTPUT
INITIAL INPUT CH 2	4	19	OUTPUT GND
INITIAL BIAS CH 2	5	18	PEAKING CONSTANT
INPUT GND	6	17	EMB STOP SW
INITIAL BIAS CH 3	7	16	$V_{CC}$
INITIAL INPUT CH 3	8	15	CHROMA OUTPUT
INITIAL INPUT CH 4	9	14	EMB OUTPUT
INITIAL BIAS CH 4	10	13	CH 3 & 4 EMB DET
HEAD AMP SW	11	12	CH 1 & 2 EMB DET

**ECG1807** 14-Pin DIP See Fig. L104  
VCR 2 Head Amp,  $V_{CC} = 5\text{ V Typ}$

$V_{CC}$	1	14	PEAKING AMP
AMP	2	13	GND
AMP 1 IN	3	12	AGC AMP OUT
N/C	4	11	AGC AMP OUT
AMP 2 IN	5	10	AGC DET
AMP	6	9	CHROMA AMP OUT
PB L	7	8	PG AMP IN

**ECG1808** 28-Pin DIP See Fig. L123A  
VCR Video Signal Processor,  $V_{CC} = 5\text{ V Typ}$

VID OUTPUT	1	28	DOC OUTPUT
PIC CTRL	2	27	ENVELOPE DET
DE-EMPHASIS	3	26	RF INPUT
PEAKING	4	25	RF OUTPUT
EXTENSION	5	24	DELAY RF INPUT
REL DET OUTPUT	6	23	$V_{CC}$
LIM OUTPUT	7	22	HPF INPUT
REC $V_{CC}$	8	21	LPF INPUT
LIM INPUT	9	20	GND
DIF SIG INPUT	10	19	LIM
DIF SIG OUTPUT	11	18	LIM
LIMIT SIG INPUT	12	17	DEM GAIN CTRL
VID INPUT	13	16	DEM OUTPUT
VID 1H INPUT	14	15	DEM 1H OUTPUT

**ECG1809** 24-Pin DIP See Fig. L122B  
VCR Capstan Drive,  $V_{CC} = 5\text{ V Typ}$

DR OUT 3	1	24	MOTOR POWER
TOTAL OUT CURR	2	23	DRIVE OUT 1
DR OUT 4	3	22	CURR PHASE COMP
BRAKE	4	21	GND
HALL IN 3	5	20	CURR DET
HALL IN 3	6	19	TORQUE LIMIT
HALL IN 2	7	18	VOLT PHASE COMP
HALL IN 2	8	17	RIPPLE CANCEL IN
HALL IN 1	9	16	TORQUE IN
HALL IN 1	10	15	REF VOLTAGE
DIR IN	11	14	HALL SUPPLY IN
DIR IN	12	13	$V_{CC}$

**ECG1810** 28-Pin DIP See Fig. L123A  
VCR Video Signal Processor,  $V_{CC} = 12\text{ V Typ}$

GND	1	28	SIF COIL
IF INPUT	2	27	SIF COIL
IF INPUT	3	26	SOUND OUTPUT
$V_{CC}$	4	25	SOUND LEVEL ADJ
IF AGC FILTER	5	24	SIF BIAS
RF AGC DELAY	6	23	SIF INPUT
RF AGC OUTPUT	7	22	SIF BIAS
LOCK DET FILTER	8	21	GND
AFC COIL	9	20	VID F CHAR COMP
AFC OUTPUT	10	19	VID OUTPUT
APC FILTER SW	11	18	VID INPUT
$V_{CC} VCO$	12	17	DET OUTPUT
APC FILTER	13	16	VCO COIL
GND VCO	14	15	VCO COIL

**ECG1811** 18-Pin DIP See Fig. L115  
VCR Servo Interface,  $V_{CC} = 5\text{ V Typ}$

PB CTRL OUTPUT	1	18	GND
PB CTRL INPUT	2	17	FG INPUT
PB CTRL AMP OUTPUT	3	16	PG INPUT
FWD/REW SEL	4	15	PG CONT
$\frac{1}{2} V_{CC}$	5	14	HEAD SW
CTRL SIGNAL	6	13	TRACKING CONT
GND	7	12	TRACKING OUTPUT
CTRL AMP FEEDBACK	8	11	$V_{CC}$
REC/PB SELECT	9	10	$\frac{1}{2} V_{SS}$ INPUT