

Linear IC and Module Circuits (cont'd)

ECG1724 10-Pin SIP See Fig. L52A
AF PO, 20 W, $V_{CC} = \pm 22$ V, $R_L = 8 \Omega$

OUTPUT 1
NC 2
+VCC 3
MUTING 4
PHASE COMPENSATION 5
PHASE COMPENSATION 6
INPUT 7
NEG FEEDBACK 8
GROUND 9
-VCC 10 ECG1724

ECG1725 16-Pin DIP See Fig. L111
TV Video/IF Amp, Det, AFT, AGC, $V_{CC} = 12$ V Typ

VIDEO OUTPUT 1
GROUND 2
AFT TANK 3
VIDEO DET TANK 4
VIDEO DET TANK 5
AFT TANK 6
AFT DEFEAT SW 7
AFT OUTPUT 8
VCC 9
AGC OUTPUT 10
BYPASS 11
IF INPUT 12
IF INPUT 13
BYPASS 14
AGC FILTER 15
AGC DELAY 16

ECG1726 24-Pin DIP See Fig. L122
TV Chroma and Vid Processor/Demod, $V_{CC} = 12$ V Typ

PIC CTRL 1
CONTRAST CTRL 2
BLACK LEVEL FIL 3
VIDEO INPUT 4
VIDEO INPUT 5
CHROMA BYPASS 6
CHROMA IN 7
GROUND 8
COLOR CTRL 9
TINT CTRL 10
BURST GATE IN 11
VCC 12
ACC FIL 13
APC FIL 14
COLOR KILLER FIL 15
OSC OUT 16
OSC IN 17
BLANK IN 18
Y AMP OUT 19
R-Y DEMOD OUT 20
G-Y DEMOD OUT 21
B-Y DEMOD OUT 22
BRIGHTNESS CTRL 23
PED CLAMP FIL 24

ECG1727 18-Pin DIP See Fig. L115
TV Signal Processor with V/H Osc/AFC Sync Sep/X-Ray Protect, $V_{CC1} = 12$ V Typ, $V_{CC2} = 10.5$ V Max

AFC REF SIGNAL IN 1
HORIZ AFC OUT 2
HORIZ HOLD 3
HORIZ OSC CAP 4
X-RAY PROT IN 5
HORIZ OUT 6
VCC 2 7
VERT OUT 8
GROUND 9
AC/DC FEEDBACK IN 10
RAMP GEN CAP 11
VERT PULSE OUT 12
VERT HOLD 13
VERT INT CAP 14
VCC 1 15
SYNC SEP OUT 16
NOISE DET IN 17
VIDEO IN 18

ECG1728 30-Pin DIP See Fig. L124C
TV VIF/SIF Amp/Det/AGC/AFT/Elect Att Audio Driver, $V_{CC} = 12$ V Typ

CERAMIC DISCRIMINATOR DE-EMPH 1
CERAMIC DISCRIMINATOR DE-EMPH 2
AF INPUT 3
NEG FEEDBACK 4
AF OUTPUT 5
GROUND 6
VID/SND MUTE SW COUPLING 7
VID/SND MUTE SW COUPLING 8
VIF INPUT 9
VIF INPUT 10
COUPLING 11
RF AGC VR 12
RF AGC OUTPUT 13
BYPASS 14
SOUND TRAP 15
SOUND TRAP 16
AFT OUTPUT 17
AFT COIL 18
LLD COIL 19
LLD COIL 20
AFT COIL 21
VIDEO OUTPUT 22
VCC 23
VCC 24
SIF OUTPUT 25
SIF INPUT 26
CERAMIC FIL 27
NEG FEEDBACK 28
MUTE ATT CTRL 29
CERAMIC DISCRIMINATOR 30

ECG1729 16-Pin DIP See Fig. L111
Pulse Width Modulator Control Ckt, $V_{CC} = 15$ V Typ

NON-INVERTED IN 1
INVERTED IN 2
FEEDBACK 3
DEAD TIME CTRL 4
CT 5
RT 6
GROUND 7
COLLECTOR 1 8
EMITTER 1 9
EMITTER 2 10
COLLECTOR 2 11
VCC 12
OUTPUT CTRL 13
VREF 14
INVERTED IN 15
NON-INVERTED IN 16

ECG1730 9-Pin SIP See Fig. L39
Dual Hi Gain Preamp, $V_{CC} = 10$ V Typ

+ INPUT 1 1
- INPUT 1 2
OUTPUT 1 3
VCC 4
GROUND 5
OUTPUT 2 6
- INPUT 2 7
+ INPUT 2 8
MUTING 9 ECG1730

ECG1731 16-Pin DIP See Fig. L111
Ten Number Repertory Dialer (CMOS), $V_{DD} = 5$ V Typ

VDD 1
TEST 2
COLUMN INPUT 3
COLUMN INPUT 4
COLUMN INPUT 5
GROUND 6
OSC INPUT 7
OSC OUTPUT 8
MAKE/BREAK SELECT 9
MUTE OUTPUT 10
ROW INPUT 11
ROW INPUT 12
ROW INPUT 13
ROW INPUT 14
HOOK SWITCH 15
PULSE OUTPUT 16

ECG1732 12-Pin SIP See Fig. L67A
135 V Regulator Plus AF PO 4 W for TV

+ DC INPUT 1
REG DRIVE 2
- DC INPUT 3
- DC INPUT 4
REG VOLT OUTPUT 5
REG VOLT OUTPUT 6
BOOTSTRAP 7
BIAS RES 8
AF OUTPUT 9
BYPASS 10
GROUND 11
AF INPUT 12 ECG1732

ECG1733 15-Pin SIP See Fig. L69A
VCR Fixed VR, 15 V @ 1 A, 9.5 V @ 1 A, 12 V @ 1 A, 5.1 V @ 0.5 A

5.1V OUTPUT 1
NC 2
12V OUTPUT 3
DECOUPLING 4
DC INPUT 2 5
12V OUTPUT 6
DC INPUT 1 7
15V OUTPUT 1 8
15V OUTPUT 1 9
9.5V OUTPUT 2 10
REFERENCE INPUT 11
GROUND 12
ON/OFF CONTROL 13
DECOUPLING 14
DECOUPLING 15 ECG1733

ECG1734 15-Pin SIP See Fig. L69A
VCR Fixed VR, 12 V @ 2 A, 9 V @ 1 A, 5.1 V @ 0.5 A

VOUT 5.5V 1
NC 2
NC 3
NC 4
NC 5
VIN 2 6
VIN 1 7
VOUT 12V 8
VOUT 12V 9
VOUT 9V 10
BIAS 11
GROUND 12
CUTOFF 13
BYPASS 14
RIPPLE FILTER 15 ECG1734

ECG1735 15-Pin SIP See Fig. L69A
VCR Fixed VR, 16 V @ 1 A, 12 V @ 1 A, 12 V @ 1.5 A, 11.9 V @ 1.5 A

11.9V OUTPUT 1
CONTROL VOLT 2
DC INPUT 2 3
REFERENCE INPUT 4
12V OUTPUT 5
REFERENCE INPUT 6
16V/12V OUTPUT 7
REFERENCE INPUT 8
DC INPUT 2 9
DC INPUT 1 10
HI/LO VOLT SWITCH 11
GROUND 12
ON/OFF CTRL 13
HEATER SWITCH 14
REFERENCE INPUT 15 ECG1735