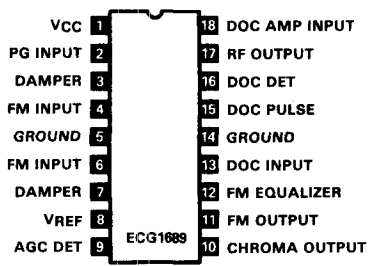
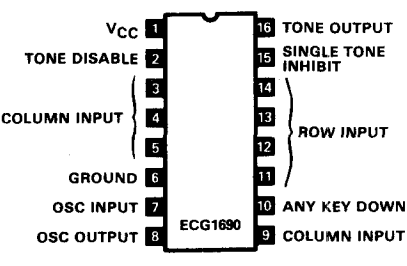
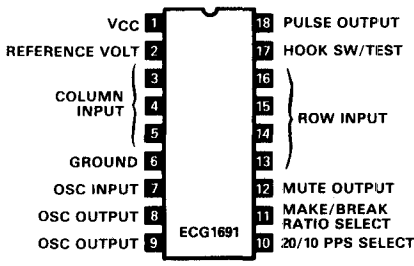
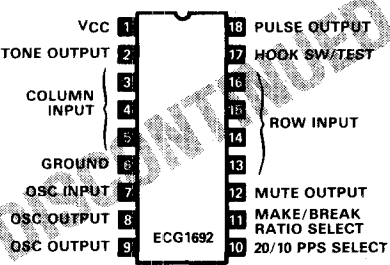
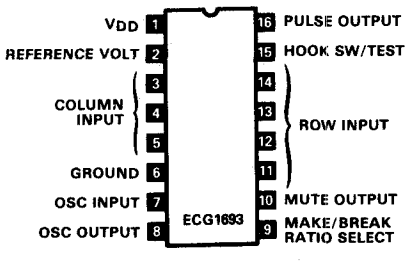


Linear IC and Module Circuits (cont'd)

<p>ECG1689 18-Pin DIP See Fig. L115 VCR Head Amp/Limiter/D.O.C./Switching, $V_{cc}=5\text{ V Typ}$</p>  <p>Pinout for ECG1689 (18-Pin DIP):</p> <ul style="list-style-type: none"> 1: VCC 2: PG INPUT 3: DAMPER 4: FM INPUT 5: GROUND 6: FM INPUT 7: DAMPER 8: VREF 9: AGC DET 10: CHROMA OUTPUT 11: FM OUTPUT 12: FM EQUALIZER 13: DOC INPUT 14: GROUND 15: DOC PULSE 16: DOC DET 17: RF OUTPUT 18: DOC AMP INPUT 	<p>ECG1690 16-Pin DIP See Fig. L111 Dual Tone Multifrequency (DTMF) Dialer (CMOS), $V_{cc}=6\text{ V Typ}$</p>  <p>Pinout for ECG1690 (16-Pin DIP):</p> <ul style="list-style-type: none"> 1: VCC 2: TONE DISABLE 3: COLUMN INPUT 4: COLUMN INPUT 5: COLUMN INPUT 6: GROUND 7: OSC INPUT 8: OSC OUTPUT 9: COLUMN INPUT 10: ANY KEY DOWN 11: ROW INPUT 12: ROW INPUT 13: ROW INPUT 14: ROW INPUT 15: SINGLE TONE INHIBIT 16: TONE OUTPUT 	<p>ECG1691 18-Pin DIP See Fig. L115 Pulse Dialer with Redial (CMOS), $V_{cc}=4\text{ V Typ}$</p>  <p>Pinout for ECG1691 (18-Pin DIP):</p> <ul style="list-style-type: none"> 1: VCC 2: REFERENCE VOLT 3: COLUMN INPUT 4: COLUMN INPUT 5: COLUMN INPUT 6: GROUND 7: OSC INPUT 8: OSC OUTPUT 9: OSC OUTPUT 10: 20/10 PPS SELECT 11: MAKE/BREAK RATIO SELECT 12: MUTE OUTPUT 13: ROW INPUT 14: ROW INPUT 15: ROW INPUT 16: ROW INPUT 17: HOOK SW/TEST 18: PULSE OUTPUT
<p>ECG1692 18-Pin DIP See Fig. L115 Pulse Dialer with Redial/Tone Signal Out (CMOS), $V_{cc}=4\text{ V Typ}$</p>  <p>Pinout for ECG1692 (18-Pin DIP):</p> <ul style="list-style-type: none"> 1: VCC 2: TONE OUTPUT 3: COLUMN INPUT 4: COLUMN INPUT 5: COLUMN INPUT 6: GROUND 7: OSC INPUT 8: OSC OUTPUT 9: OSC OUTPUT 10: 20/10 PPS SELECT 11: MAKE/BREAK RATIO SELECT 12: MUTE OUTPUT 13: ROW INPUT 14: ROW INPUT 15: ROW INPUT 16: ROW INPUT 17: HOOK SW/TEST 18: PULSE OUTPUT 	<p>ECG1693 16-Pin DIP See Fig. L111 Push Button Pulse Dialer with Redial (CMOS), $V_{DD}=6\text{ V Max}$</p>  <p>Pinout for ECG1693 (16-Pin DIP):</p> <ul style="list-style-type: none"> 1: VDD 2: REFERENCE VOLT 3: COLUMN INPUT 4: COLUMN INPUT 5: COLUMN INPUT 6: GROUND 7: OSC INPUT 8: OSC OUTPUT 9: MAKE/BREAK RATIO SELECT 10: MUTE OUTPUT 11: ROW INPUT 12: ROW INPUT 13: ROW INPUT 14: ROW INPUT 15: HOOK SW/TEST 16: PULSE OUTPUT 	