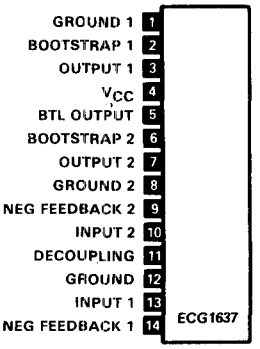
Linear IC and Module Circuits (cont'd)

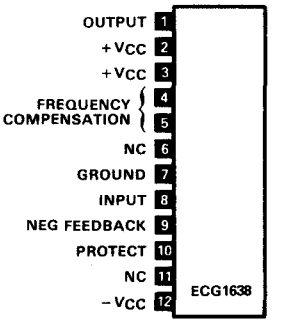
ECG1637 14-Pin SIP See Fig. L58

Dual 4.2 W (Typ)/Ch AF PO, V_{CC} = 12 V Typ, R_L = 4 Ω



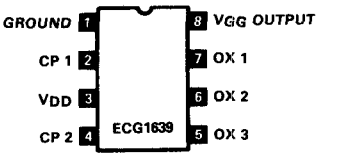
ECG1638 12-Pin SIP See Fig. L57

6.5 W (Typ) AF PO, V_{CC} = ±12 V Typ, R_L = 8 Ω



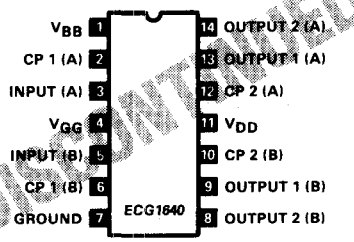
ECG1639 8-Pin DIP See Fig. L97

Clock Generator/Driver for BBD's, V_{DD} = 15 V Typ



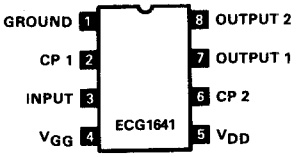
ECG1640 14-Pin DIP See Fig. L104

Audio Signal Delay, Dual 512 Stage BBD, .32 to 25.6 msec/Stage, V_{DD} = −15 V Typ, V_{GG} = −14 V, V_{BB} = +5 V



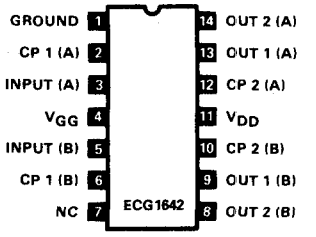
ECG1641 8-Pin DIP See Fig. L97

Audio Signal Delay, 1024 Stage Low Noise BBD, 5.12 to 51.2 msec Delay, V_{DD} = −15 V Typ, V_{GG} = −14 V Typ



ECG1642 14-Pin DIP See Fig. L104

Audio Signal Delay, Dual 512 Stage BBD (PMOS) Delay to 25.6 msec/Stage, V_{DD} = −5 V Typ, V_{GG} = −14 V Typ

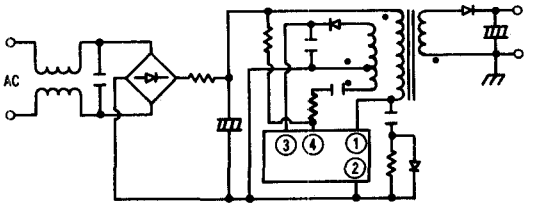


ECG1644 TO-3, 3-Pin See Fig. L11A

Hybrid Self Exciting Converter, V_{IN} = 120 VAC

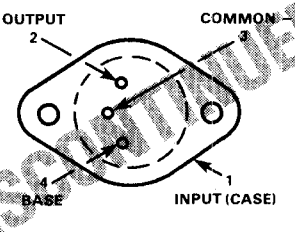
ECG1645

Hybrid Self Exciting Converter, V_{IN} = 240 VAC



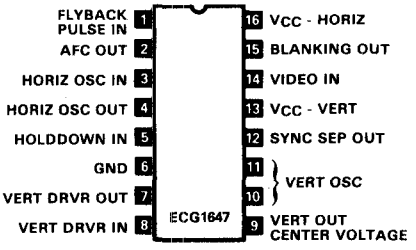
ECG1646 TO-3, 3-Pin See Fig. L11A

TV Voltage Regulator, Output = +129 V DC, 1 A



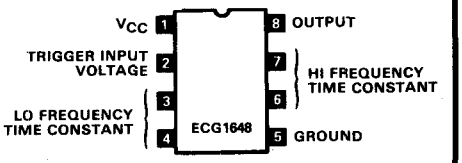
ECG1647 16-Pin DIP See Fig. L111

TV Horiz/Vert Osc/Driver/Sync Sep, AFC, V_{CC} = 12 V Typ



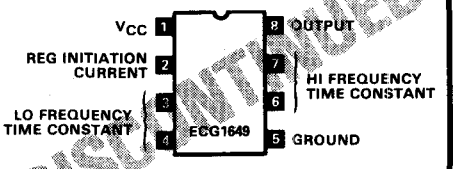
ECG1648 8-Pin DIP See Fig. L97

Telephone Tone Ringer, Programmable Threshold, with Logic Inhibit, V_{CC} = 15 V Typ



ECG1649 8-Pin DIP See Fig. L97

Telephone Tone Ringer - Current Initiated, V_{CC} = 19 V Typ



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Package Outlines - See Page 1-269