

# Linear IC and Module Circuits (cont'd)

**ECG1312** 14-Pin DIP See Fig. L104  
Vert Osc, Amp,  $V_{CC} = 12\text{ V Typ}$

NC 1 14 NC  
NC 2 13 NC  
TIME CONSTANT { 3 12 VERT SYNC IN  
4 11 VERT FREQ CTRL  
VCC 5 10 VERT OSC OUTPUT  
GROUND 6 9 NEG FEEDBACK  
NC 7 8 NC

ECG1312

**ECG1314** 18-Pin DIP See Fig. L115  
VCR Sync Sep/AFC/90° Rotary Circuit,  $V_{CC} = 12\text{ V DC Typ}$

Vz/7.2V MAX 1 18 HD OUT  
VCC 2 17 RC NETWORK/MM  
SYNC SEP OUT 3 16 FILTER  
SYNC FRONT PULSE OUT 4 15 PL INJECTOR  
FILTER 5 14 VCO CONTROL  
SYNC SEP IN 6 13 GROUND  
WHITE CLIP OUT 7 12 CW OUT  
VIDEO IN 8 11 PG IN  
REC/DOC SELECT 9 10 ID IN

ECG1314

**ECG1315** 18-Pin SIP-M See Fig. L75D  
Dual AF PO, 35 W,  $V_{CC} = \pm 30\text{ V}$ ,  $R_L = 8\ \Omega$

**ECG1316**  
Dual AF PO, 50 W,  
 $V_{CC} = \pm 35\text{ V}$ ,  $R_L = 8\ \Omega$

12 11

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18

**ECG1317** 18-Pin SIP-M See Fig. L75B  
Dual AF PO, 50 W,  $V_{CC} = \pm 35.5\text{ V}$ ,  $R_L = 8\ \Omega$

12 11

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18

**ECG1318** 10-Pin SIP See Fig. L53B  
VCR Pos DC VR, Dual Output, 9.5 V @ 1.6 A,  
15 V @ 2.5 A

GND 1  
VO 1/9.5 V 2  
BIAS 3  
VO 2/15 V { 4  
5  
VB 6  
VIN DC 7  
CUTOFF { 8  
9  
GND 10

ECG1318

**ECG1319** 10-Pin SIP See Fig. L53A  
Mod-AF PO, 15 W,  $V_{CC} = 38\text{ V DC Typ}$ ,  
 $R_L = 8\ \Omega$

RIPPLE FILTER 1  
BYPASS 2  
GROUND 3  
FEEDBACK 4  
AF INPUT 5  
FEEDBACK 6  
BOOTSTRAP 7  
GROUND 8  
AF OUTPUT 9  
VCC 10

ECG1319

**ECG1320** 10-Pin SIP-M See Fig. L63  
AF PO, 25 W,  $V_{CC} = \pm 25\text{ V Typ}$

INPUT 1  
-VCC 2  
OUTPUT 3  
OUTPUT 4  
NC { 5  
6  
7  
OUTPUT 8 ECG1279  
+VCC 9 ECG1280  
INPUT 10 ECG1282  
ECG1320

**ECG1321** 10-Pin SIP-M See Fig. L64  
Mod-AF PO, 55 W,  $V_{CC} = \pm 38\text{ V}$

-INPUT 1  
-VCC 2  
RE 1 3  
NC 4  
NC 5  
NC 6  
NC 7  
RE 2 8  
+VCC 9  
+INPUT 10

ECG1321  
ECG1342

**ECG1322** 10-Pin SIP-M See Fig. L62  
AF PO, 80 W,  $V_{CC} = \pm 46\text{ V Typ}$

INPUT + 1  
VCC - 2  
OUTPUT 3  
OUTPUT 4  
NC { 5  
6  
7  
OUTPUT 8  
VCC + 9  
INPUT - 10

ECG1281  
ECG1322

**ECG1323** 10-Pin SIP-M See Fig. L63  
Mod-AF PO, 15 W,  $V_{CC} = 38\text{ V Typ}$

GROUND 1  
OUTPUT 2  
VCC 3  
BOOTSTRAP 4  
RIPPLE FILTER 5  
INPUT 6  
NEG FEEDBACK 7  
BYPASS 8  
PRE-DRIVER OUT 9  
DRIVER INPUT 10

ECG1323