

Linear IC and Module Circuits (cont'd)

ECG1200 16-Pin DIP See Fig. L112
TV Chroma Processor, $V_{CC} = 11.2$ V Typ

CHROMA INPUT 1
SAMPLE HOLD PHASE SHIFT 2
AF PC DET 4
GROUND 5
3.58 MHZ 6
AMP LIMITER 7
OSC OUTPUT 8

16 MANUAL CTRL
15 CHROMA OUTPUT
14 CHROMA CTRL
13 OVERLOAD DET
12 V_{CC}
11 SAMPLE HOLD TRANSLATOR
10 KEYSER

ECG1201, ECG1204 10-Pin SIP See Fig. L43
CB Voltage Controlled Oscillator

ECG1202 10-Pin SIP See Fig. L43
CB Voltage Controlled Oscillator

ECG1203 10-Pin SIP See Fig. L43
CB Voltage Controlled Oscillator

ECG1205 16-Pin DIP See Fig. L112
Tape Recorder Preamp, $V_{CC} = 5$ V

ISO TRANS { BASE 1
COLL 2
GROUND 3
COMP 4
EMITTER BYPASS/FEEDBACK 5
PREAMP IN 6
PREAMP OUT 7
PREAMP/VCC 8

16 PREAMP BYPASS/FEEDBACK
15 GROUND
14 NFB/BYPASS
13 PREDRIVER IN
12 PREDRIVER OUT
11 DRIVER IN
10 DRIVER COLLECTOR
9 PREDRIVER/VCC

ECG1206 14-Pin DIP See Fig. L105
PLL Stereo Decoder

VCC 1
INPUT 2
AMP OUTPUT 3
LEFT OUTPUT 4
RIGHT OUTPUT 5
ST IND 6
GROUND 7

14 VCO ADJ
13 LPF
12 LPF
11 DET INPUT
10 19 KHZ TP
9 LPF
8 LPF

ECG1208 9-Pin SIP See Fig. L39
CMOS Phase Comparator

A OUTPUT 1
A INPUT 2
PD OUTPUT 3
PHASE OUTPUT 4
VDD 5
NC 6
S 7
R 8
VSS 9

ECG1209 8-Pin DIP See Fig. L97
4-Bit Binary Counter

COUNT IN 1
GROUND 2
VCC 3
RESET 4

8 A (1)
7 D (8)
6 B (2)
5 C (4)

ECG1210 9-Pin SIP See Fig. L40
Audio Preamp

BIAS 1
INPUT 2
BYPASS 3
ALC INPUT 4
GROUND 5
ALC OUTPUT 6
NFB 7
OUTPUT 8
VCC 9

ECG1211 10-Pin SIP-HS See Fig. L88
5.5 W Audio Out, $V_{CC} = 13.2$ V, $R_L = 4 \Omega$

GROUND 1
POWER OUTPUT 2
VCC 3
BOOTSTRAP 4
PHASE COMP 5
NC 7
FILTER CAP 8
NFB 9
AUDIO INPUT 10

N