

Linear IC and Module Circuits (cont'd)

ECG1167 16-Pin DIP See Fig. L111
CMOS PLL Frequency Synthesizer

VDD 1 16 GROUND
PIN 2 15 P₀
R_{IN} 3 14 P₁
NC 4 13 P₂
D_O 5 12 P₃
L_D 6 11 P₄
P₈ 7 10 P₅
P₇ 8 9 P₆
ECG1167

ECG1168 16-Pin DIP See Fig. L111
Video Processor, V_{CC} = 12 V Typ

VCC 1 16 SYNC SEP OUT
BEAM CURRENT LIMITER 2 15 SYNC SEP IN
RF AGC OUT 3 14 SYNC AMP OUT
RF AGC DELAY ADJ 4 13 VIDEO AMP OUT
IF AGC OUT 5 12 VIDEO AMP OUT
FILTER 6 11 VIDEO AMP IN
AGC DET IN 7 10 VIDEO AMP IN
ECG1168 9 GROUND

ECG1169 10-Pin SIP-HS See Fig. L83
Audio Output/Modulator, V_{CC} = 13.2 V

VCC 1
FILTER 2
PHASE COMPENSATION 3
PHASE COMPENSATION 4
INPUT 5
RIPPLE FILTER 6
NEG FB 7
BOOTSTRAP 8
GROUND 9
OUTPUT 10
ECG1169

ECG1170 8-Pin SIP See Fig. L33
Audio Preamp (Dual)

CHAN 2 INPUT 1
CHAN 2 FEEDBACK 2
CHAN 2 OUTPUT 3
VCC 4
GROUND 5
CHAN 1 OUTPUT 6
CHAN 1 FEEDBACK 7
CHAN 1 INPUT 8
ECG1170

ECG1171 8-Pin Can See Fig. L3
Op Amp

INV INPUT - 1
INV INPUT - 2
NON-INV INPUT + 3
CASE 4
OFFSET NULL 5
OUTPUT 6
V+ 7
C2 8
C1
ECG1171

ECG1172 14-Pin DIP See Fig. L104
Phase Frequency Detector for PLL

REFERENCE IN 1 14 VCC
DOWN OUTPUT 1 2 13 UP OUTPUT 1
VARIABLE IN 3 12 UP OUTPUT 2
CHAR PUMP UP IN 4 11 CHAR PUMP DOWN IN
CHAR PUMP UP OUTPUT 5 10 CHAR PUMP DOWN OUTPUT
DOWN OUTPUT 2 6 9 FILTER AMP IN
GROUND 7 8 FILTER AMP OUTPUT
ECG1172

ECG1173 16-Pin DIP See Fig. L112
TV Video IF Amp and AGC, V_{CC} = 12 V Typ

IF OUT 1 16 IF OUT
DECOUPLING 2 15 GROUND
IF IN 3 14 VCC
RF AGC AMP OUT 4 13 BYPASS
FILTER 5 12 IF AMP IN/AGC
VIDEO INPUT 6 11 RF AGC DELAY ADJ
VIDEO INPUT 7 10 IF AGC ADJ
VIDEO INPUT 8 9 IF AGC OUT
ECG1173

ECG1174 14-Pin DIP-Q See Fig. L107
TV Automatic Fine Tuning

NC 1 14 DC AMP B OUTPUT
NC 2 13 DC AMP A OUTPUT
INTERCARRIER OUTPUT 3 12 FILTER A
GND SUBSTRATE 4 11 FILTER B
IF INPUT 5 10 DET B INPUT
DET BIAS 6 9 IF AMP OUTPUT
SHUNT REG 7 8 DET A INPUT
ECG1174

ECG1177 14-Pin DIP See Fig. L104
TV Luminance Processor

VIDEO IN 1 14 NC
PEAKING IN 2 13 SHUNT REG. BIAS
PEAKING IN 3 12 CLAMP INHIB IN
VIDEO OUT 4 11 PEAKING CTRL
GND SUBSTRATE 5 10 CONTRAST CTRL
CLAMP IN 6 9 BLANKING IN
CLAMPED VID OUT 7 8 BRIGHTNESS CTRL
ECG1177

ECG1179 8-Pin DIP See Fig. L97
Audio Preamp, V_{CC} = 5.5 V Typ

ALC OUT 1 8 ALC INPUT
BYPASS 2 7 GROUND
FEEDBACK 3 6 PREAMP OUT/VCC
PREAMP INPUT 4 5 DECOUPLED VCC
ECG1179